

FIG. 1

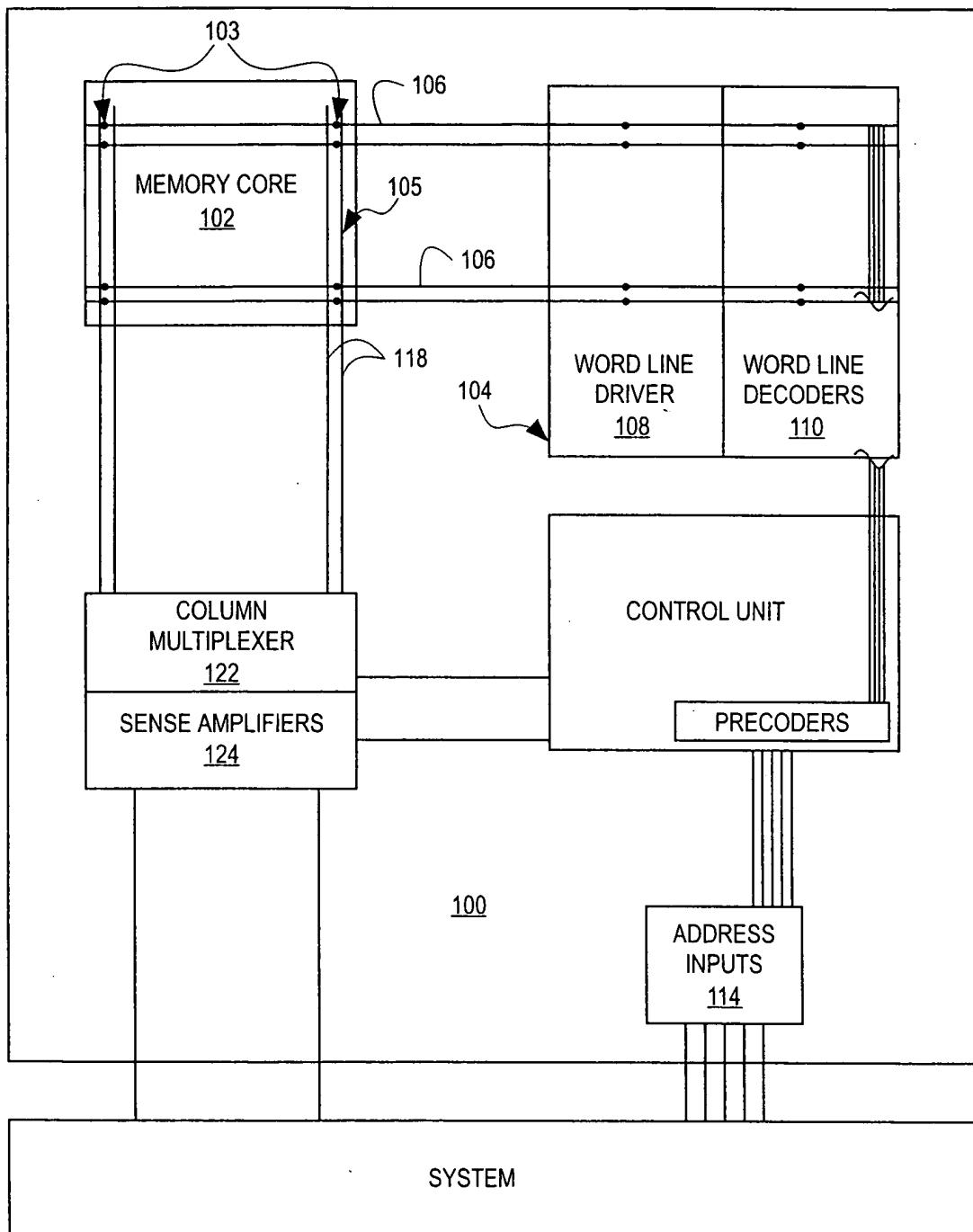
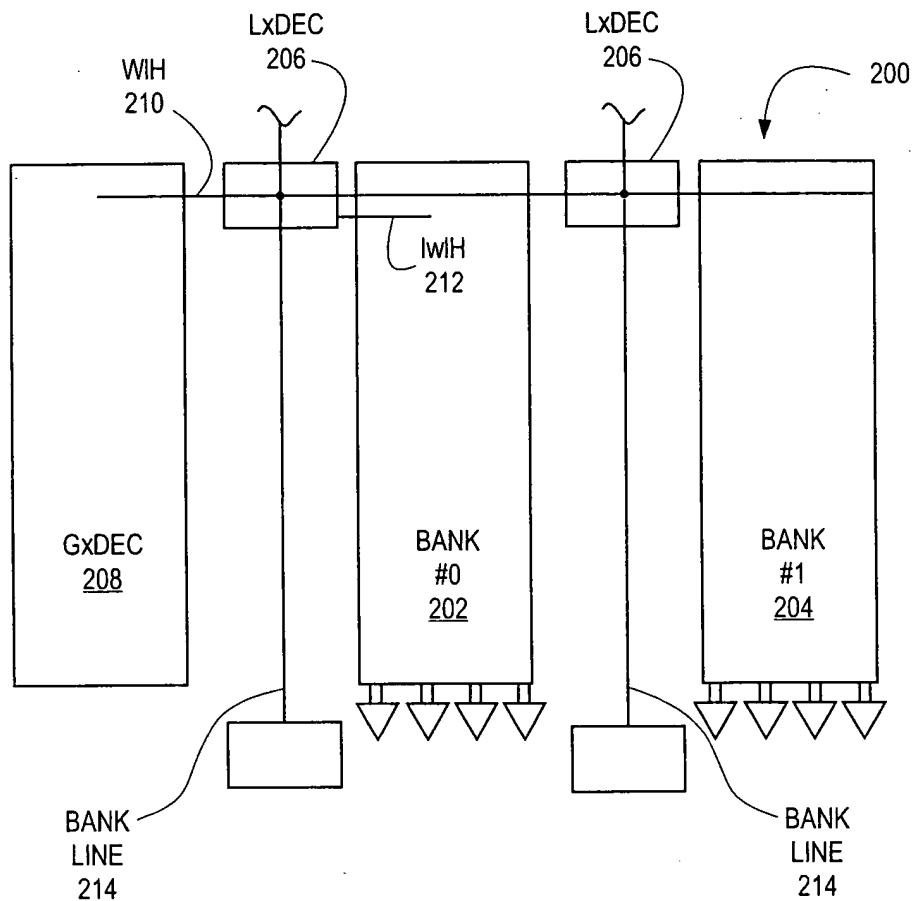


FIG. 2



TITLE: Block Redundancy Implementation In
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INVENTOR: Terzioglu, et al.
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ATTORNEY: TLH, PHONE: 312-775-8000

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FIG. 3A

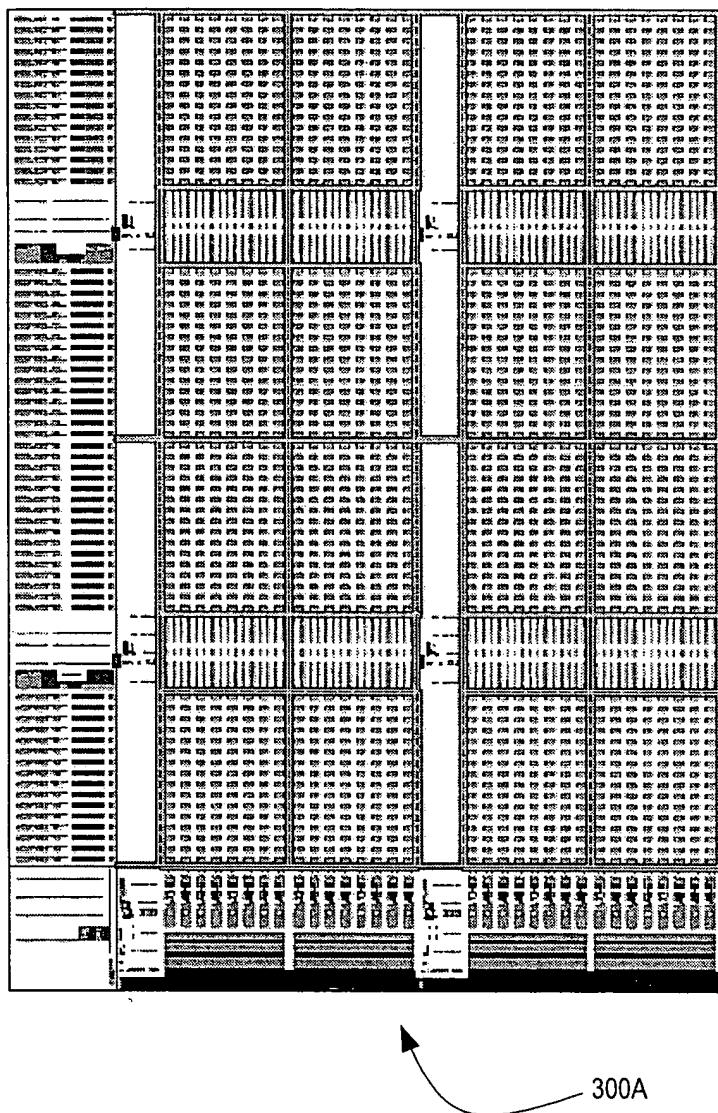
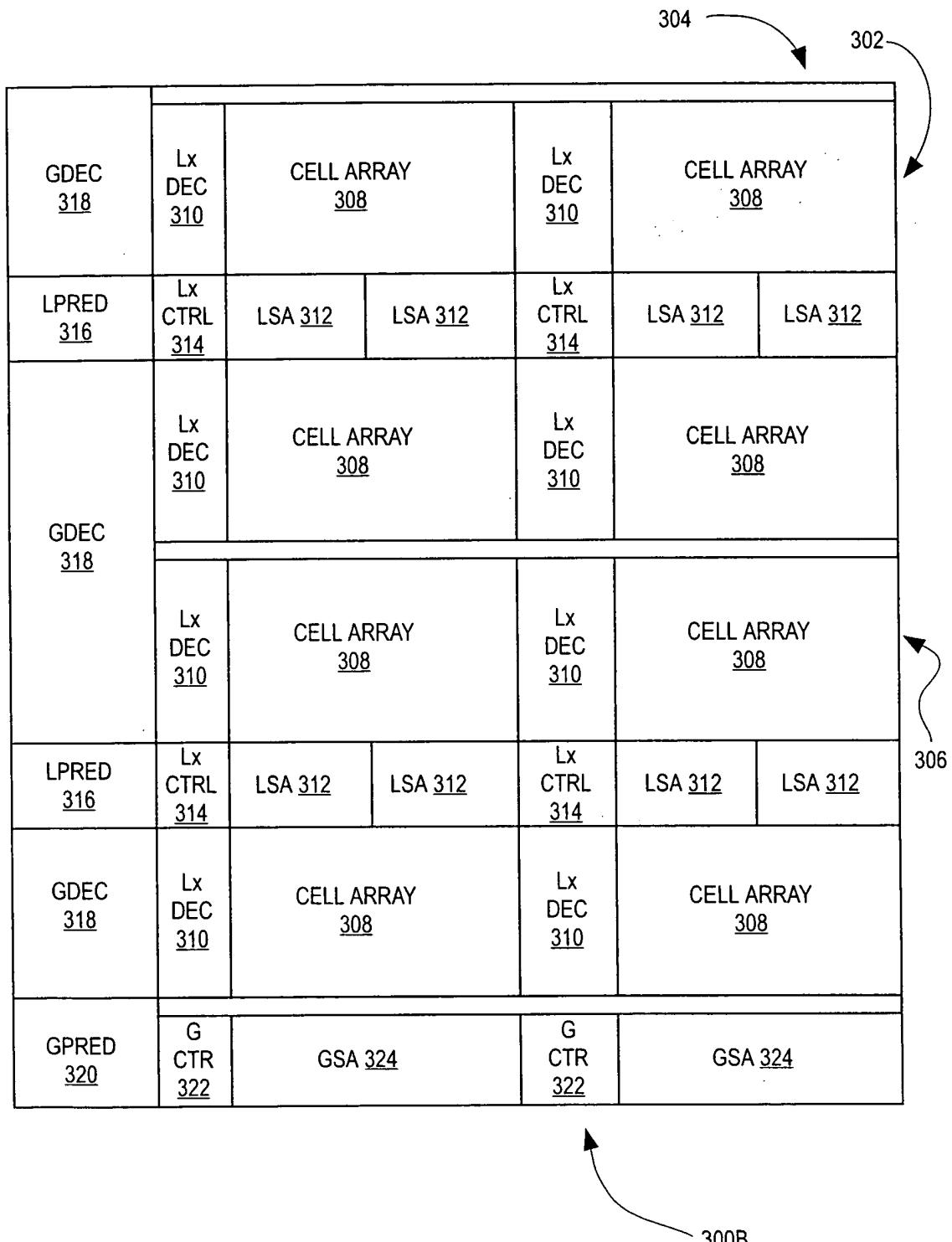
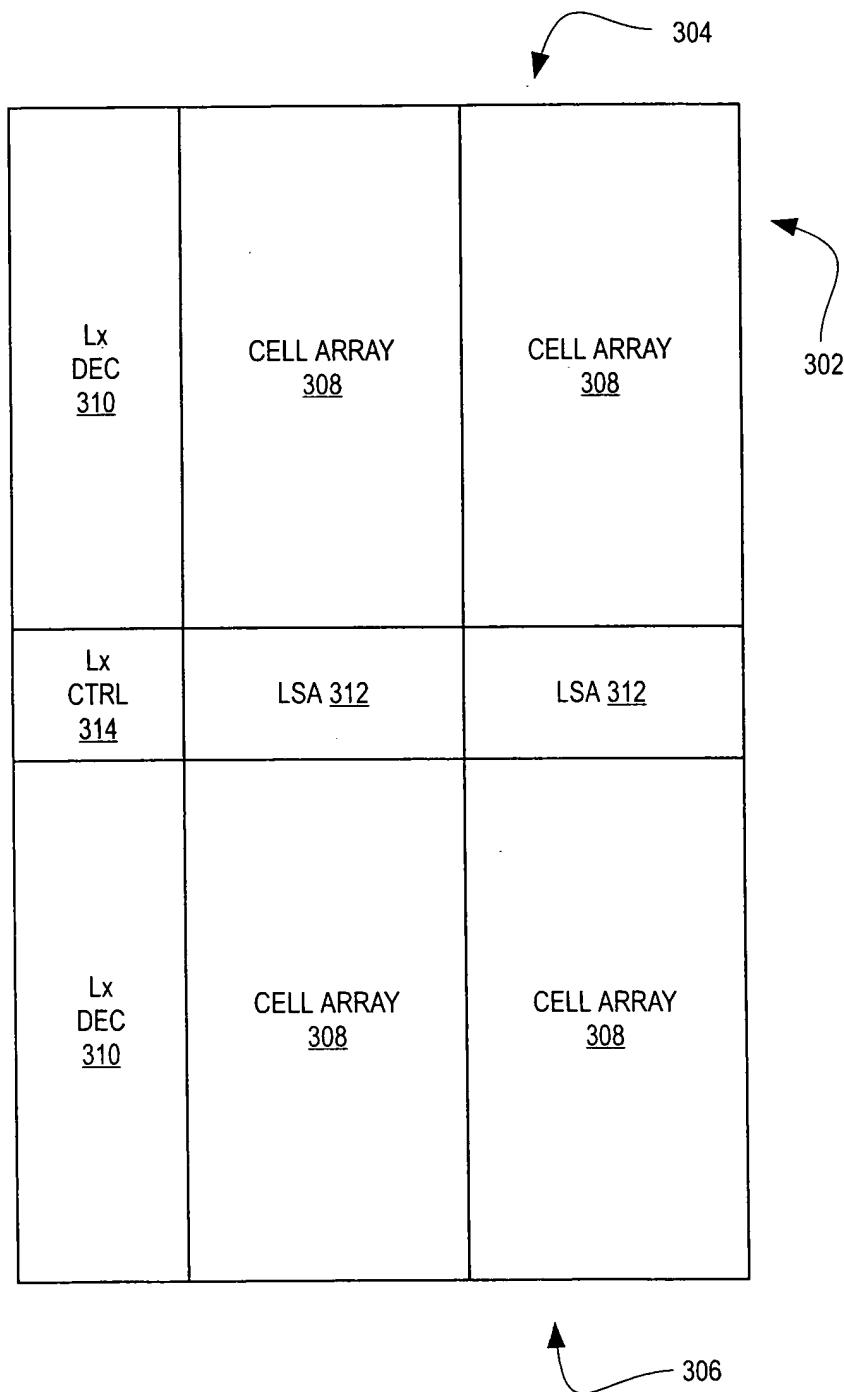


FIG. 3B



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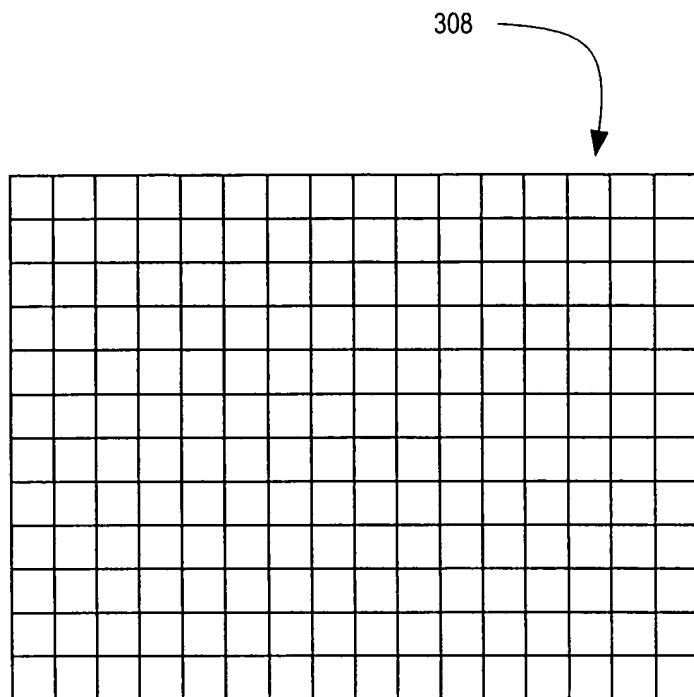
FIG. 4



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FIG. 5



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FIG.6A

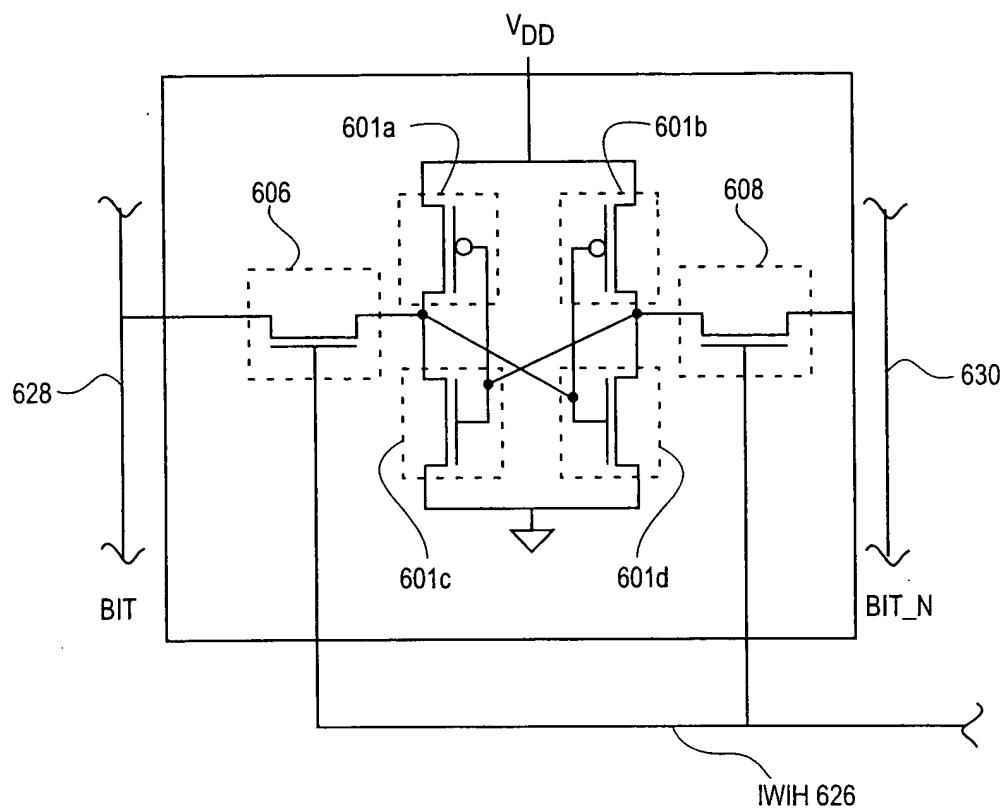


FIG.6B IWIH 626

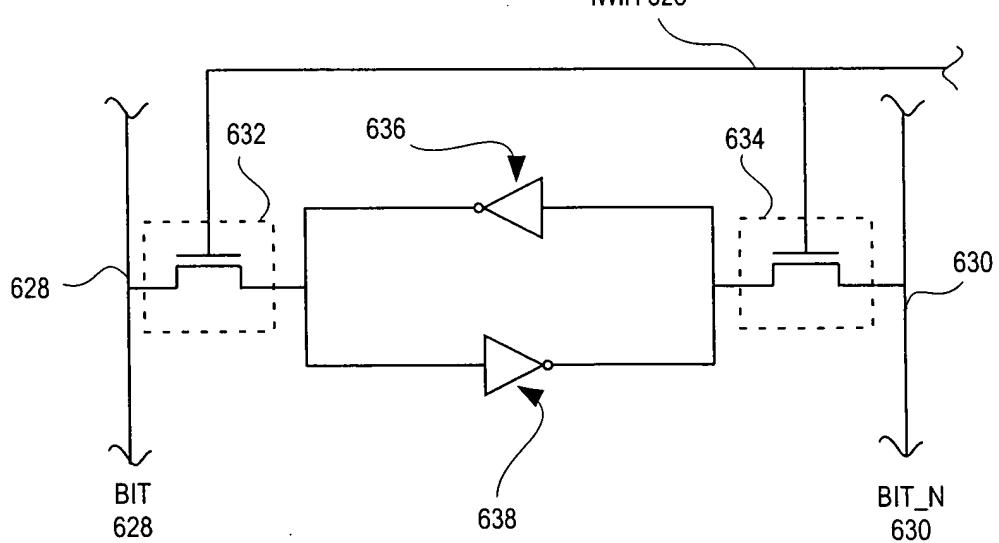
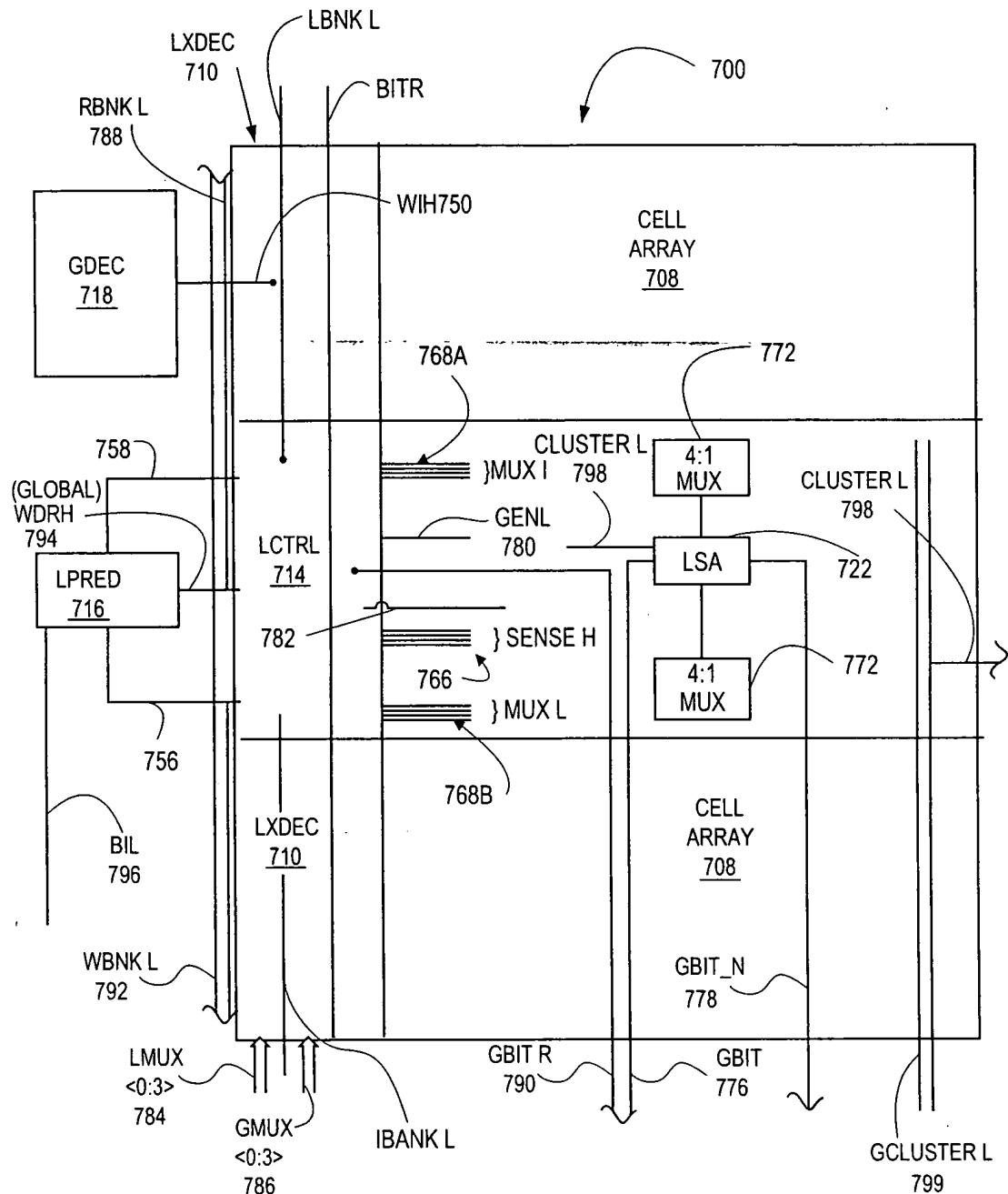


FIG. 7



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FIG. 8

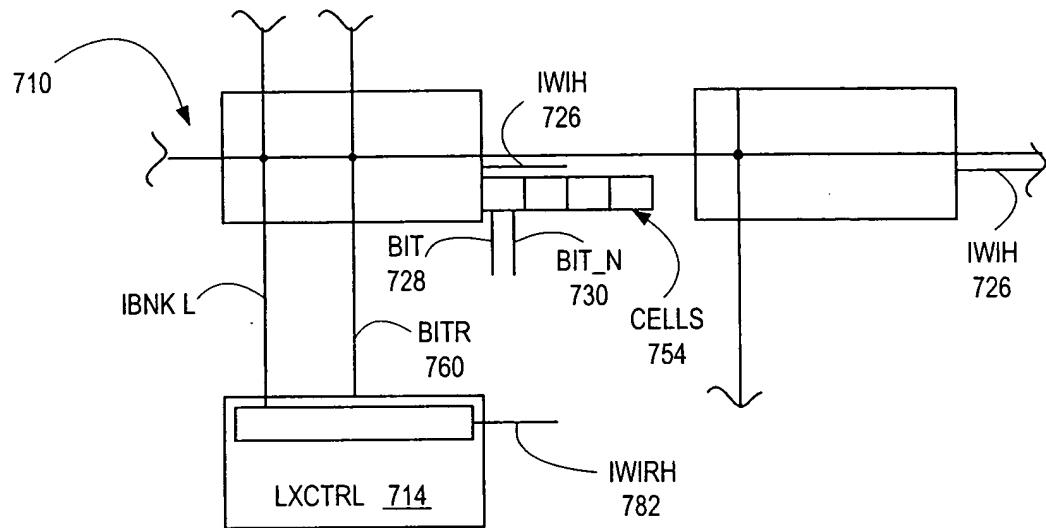
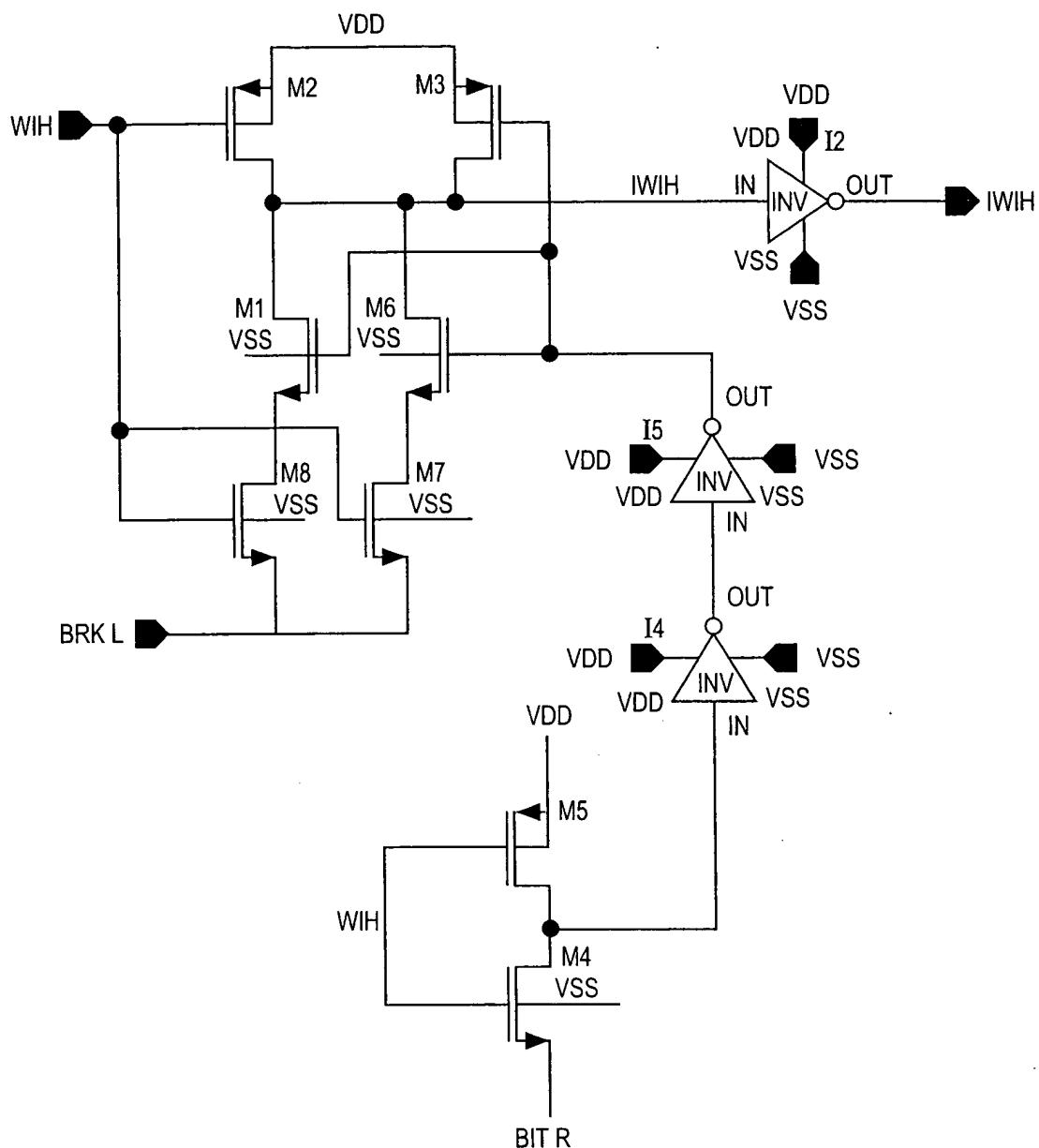
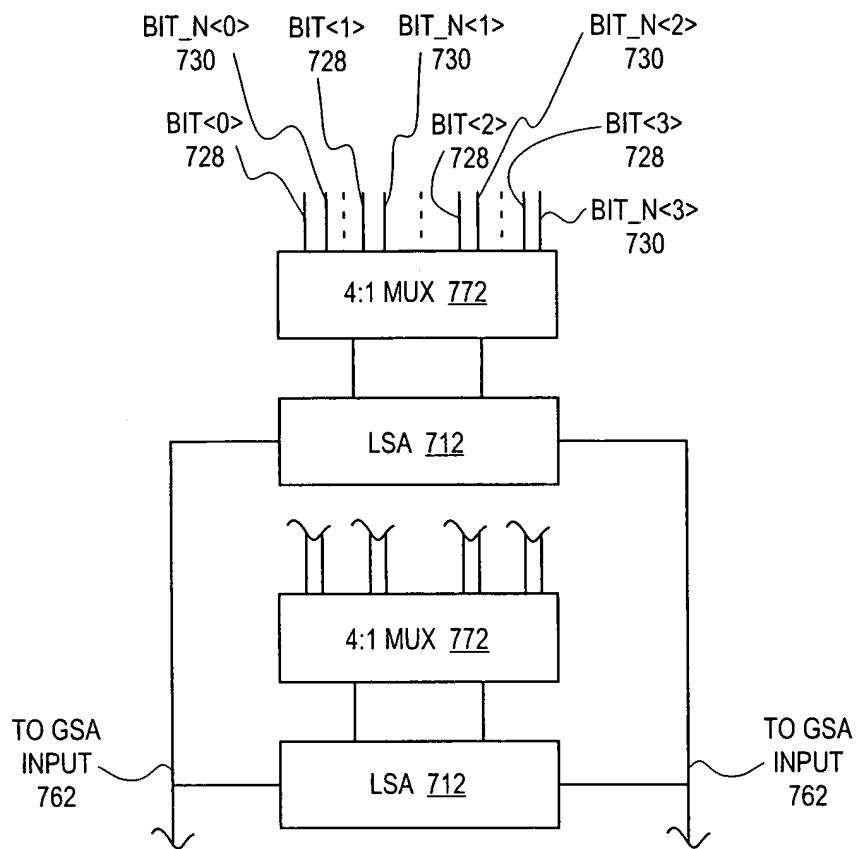


FIG. 9



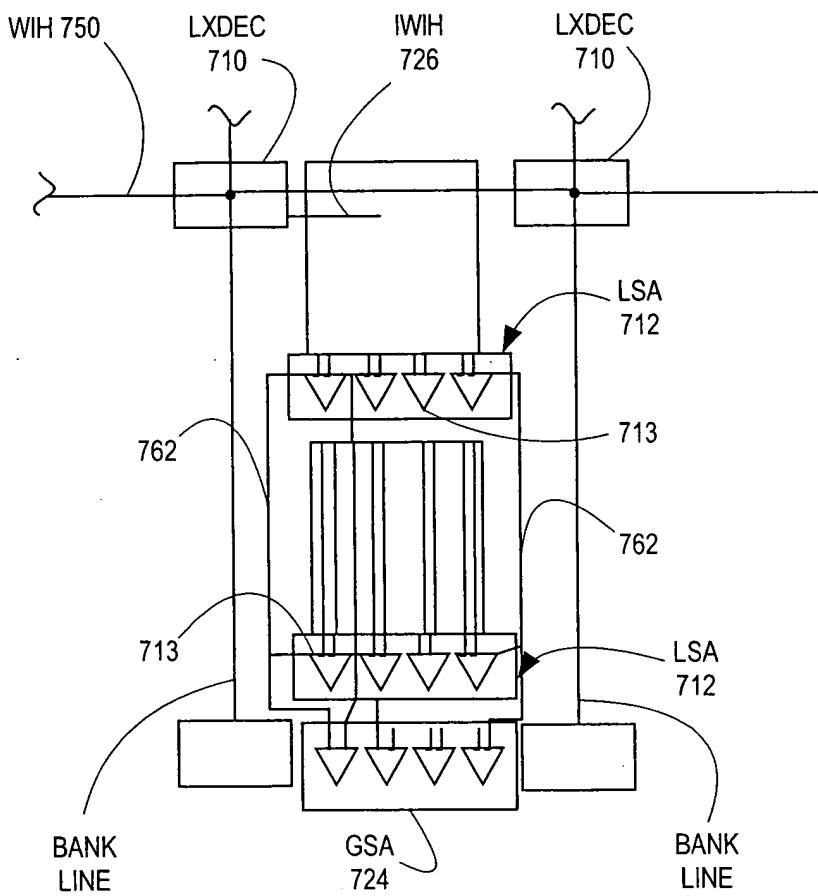
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FIG. 10



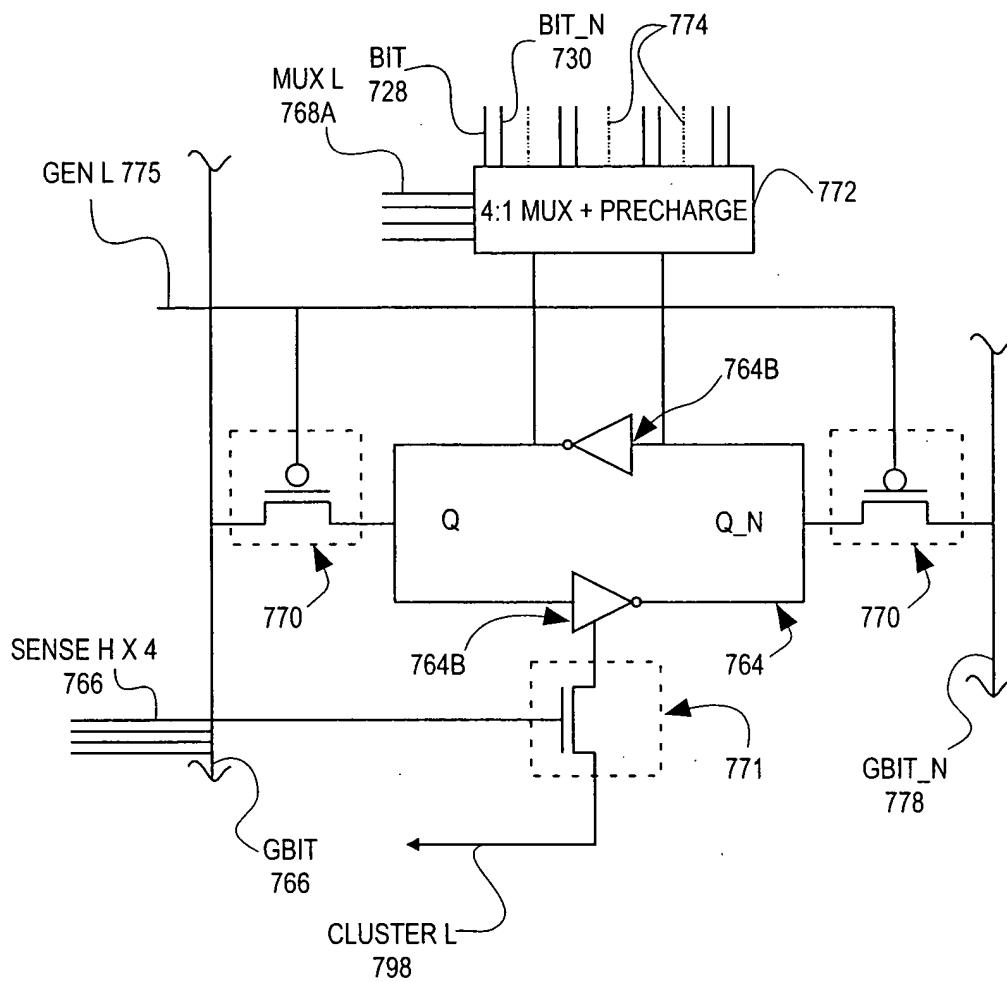
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FIG.11



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FIG. 12A



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FIG. 12B

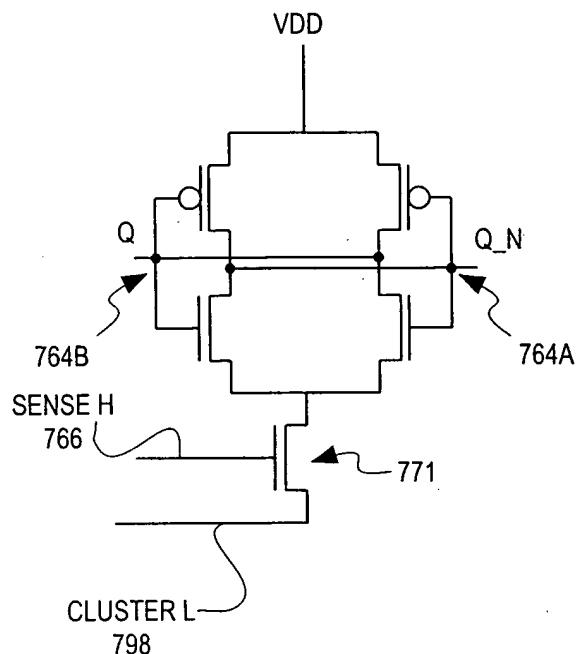
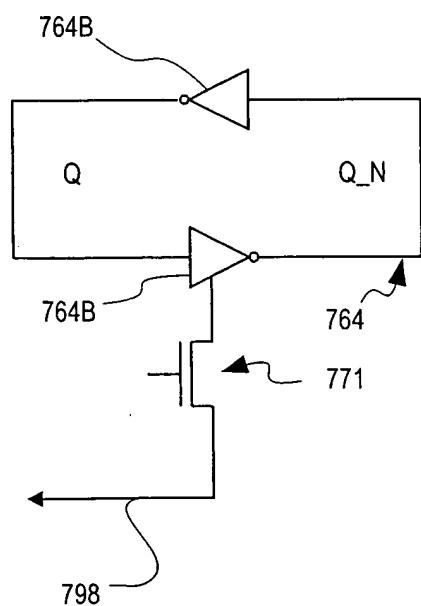


FIG. 12C



TITLE: Block Redundancy Implementation In
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FIG. 13

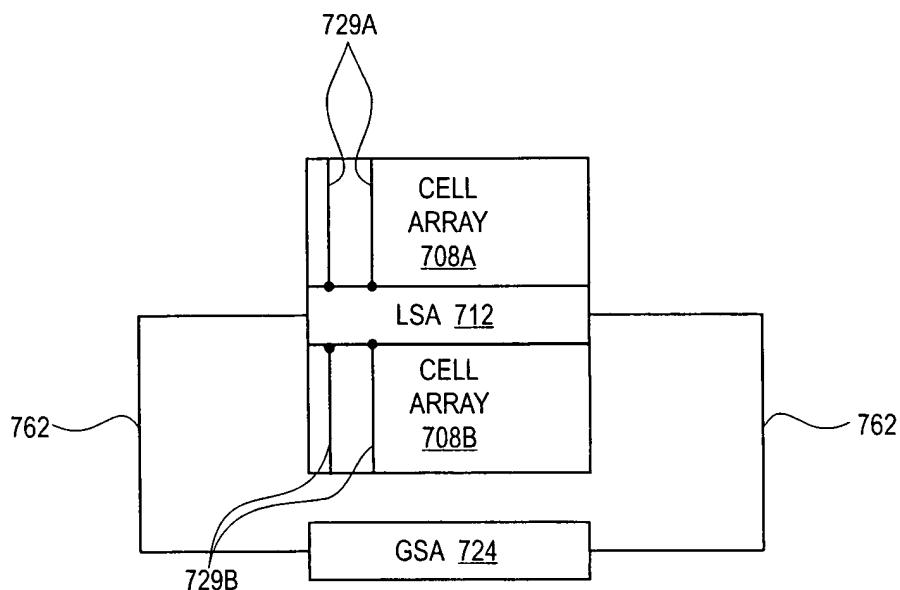
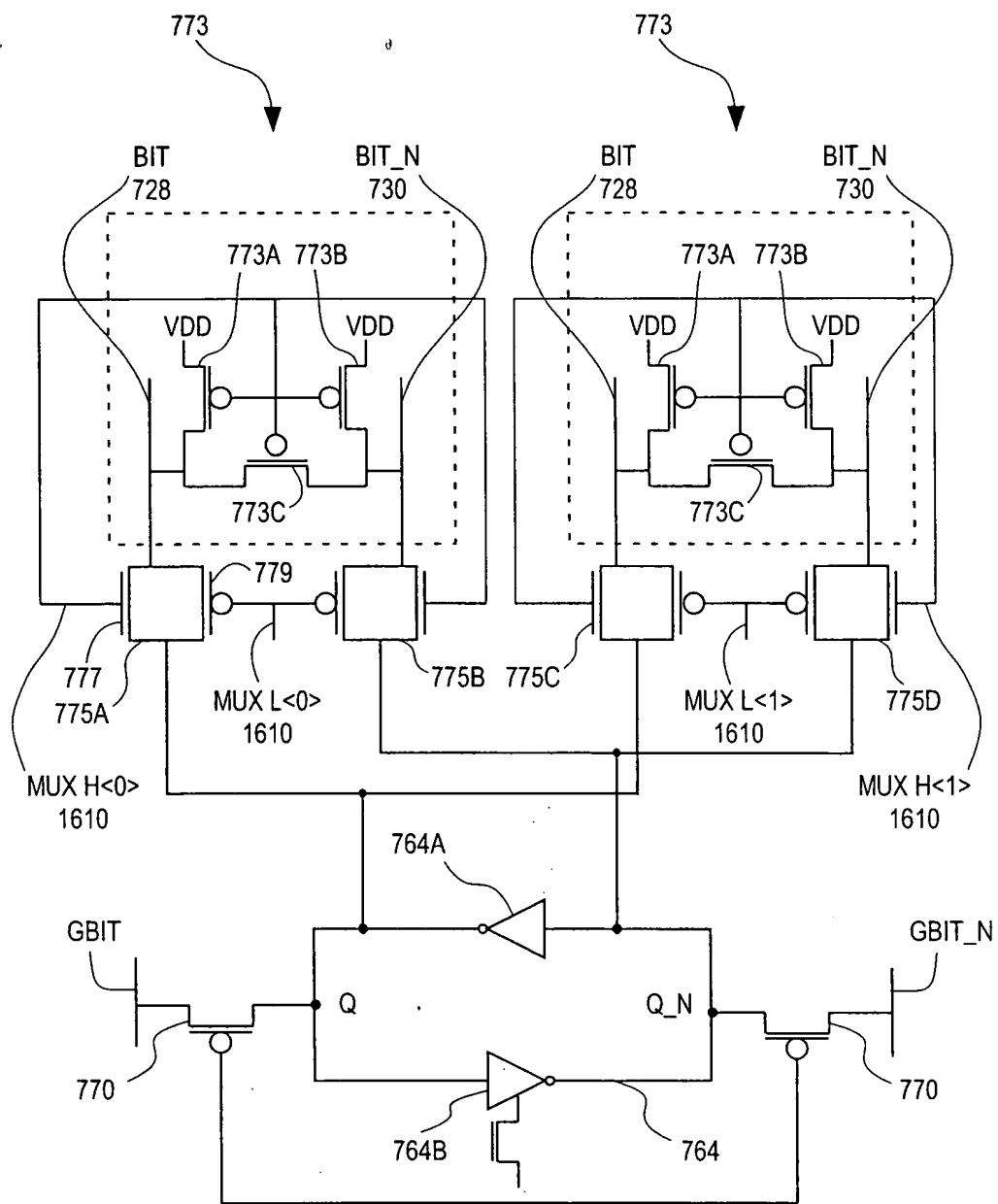


FIG. 14



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FIG. 15

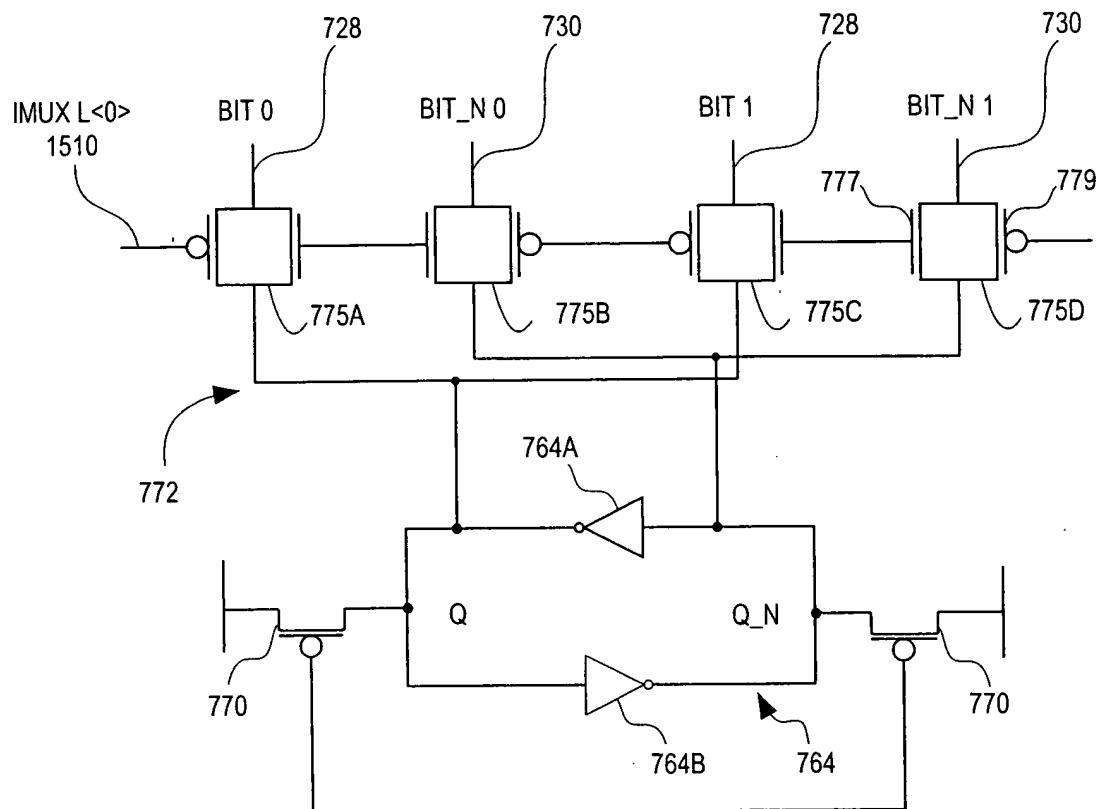
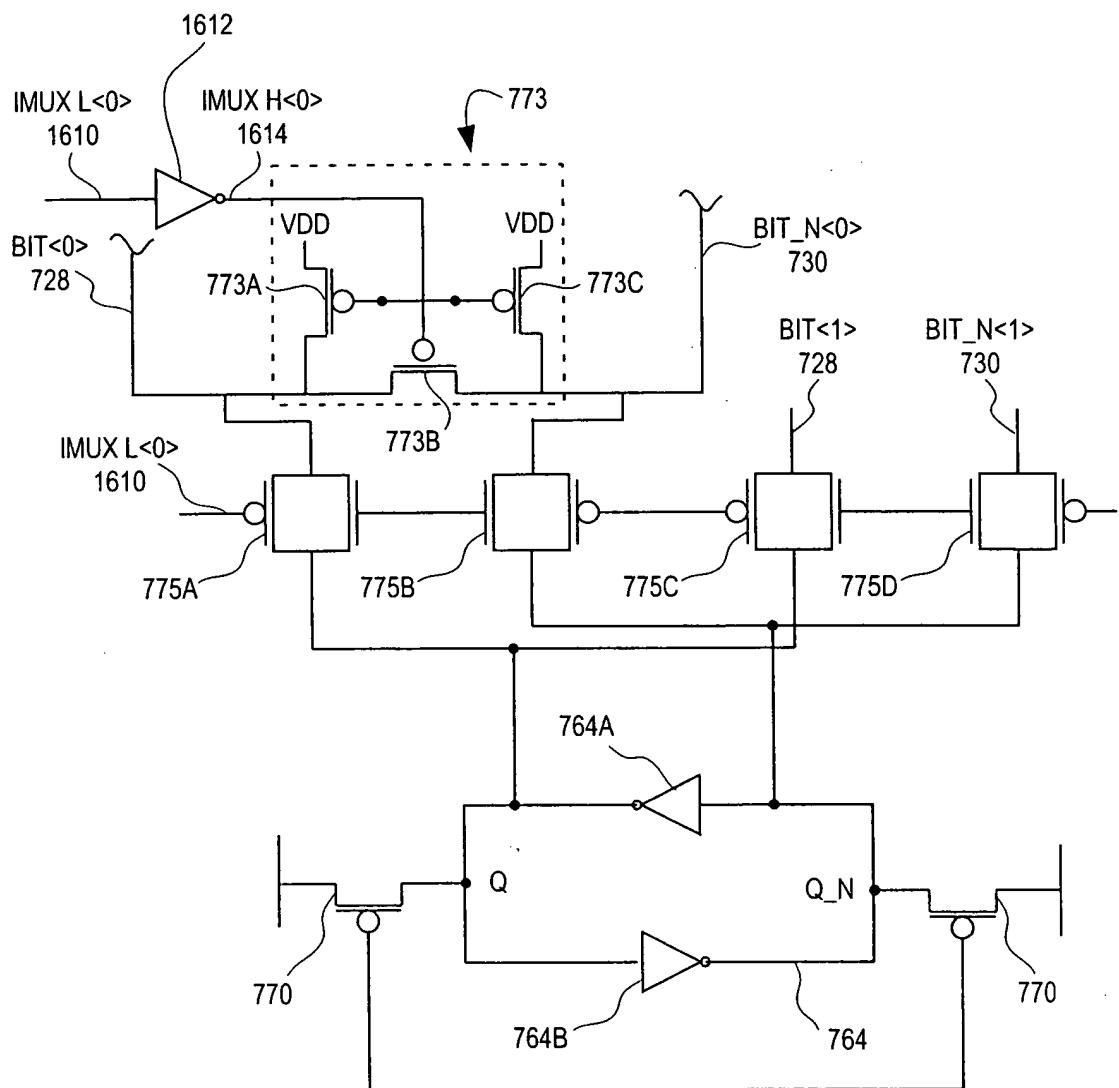
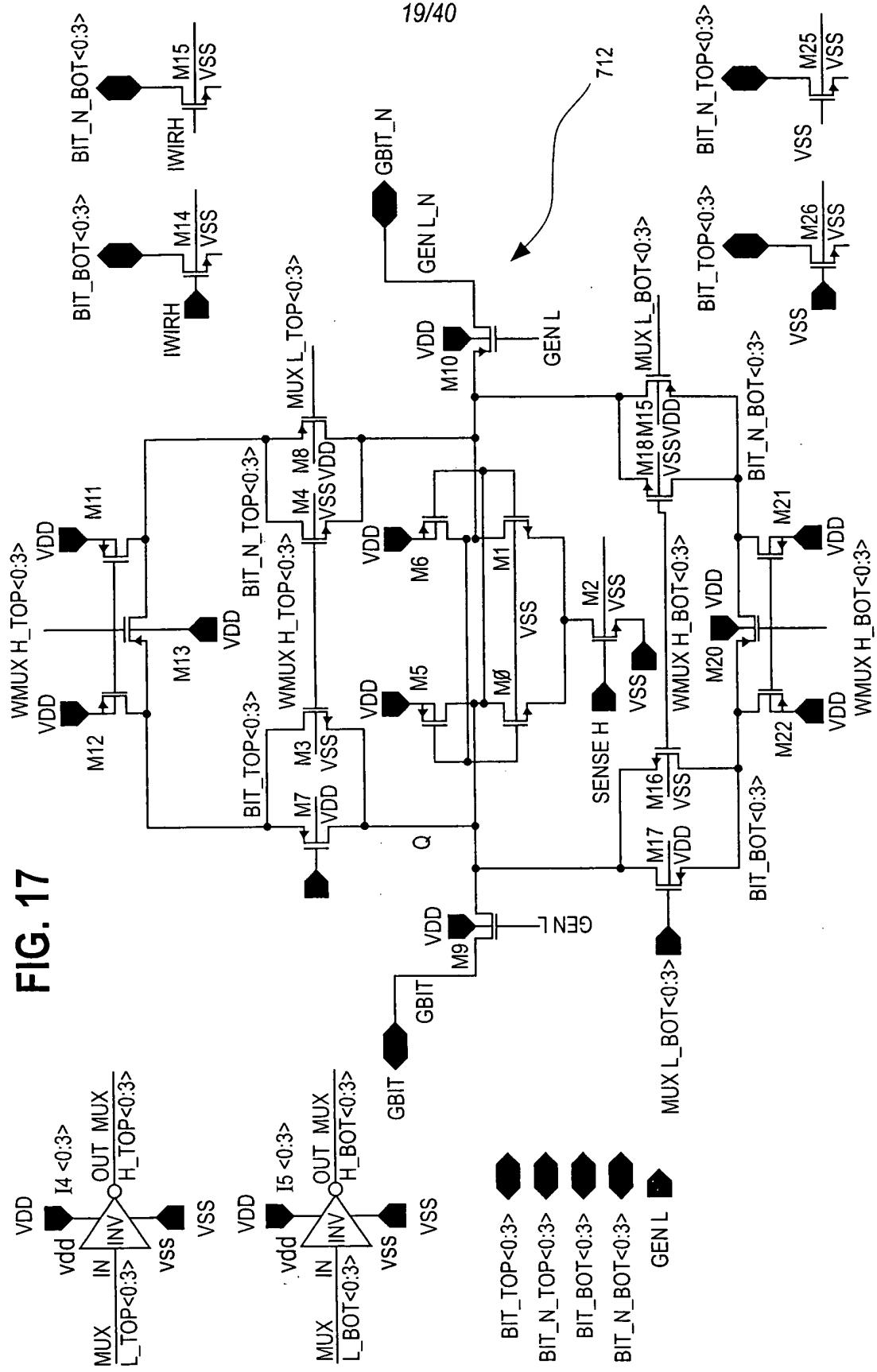


FIG. 16

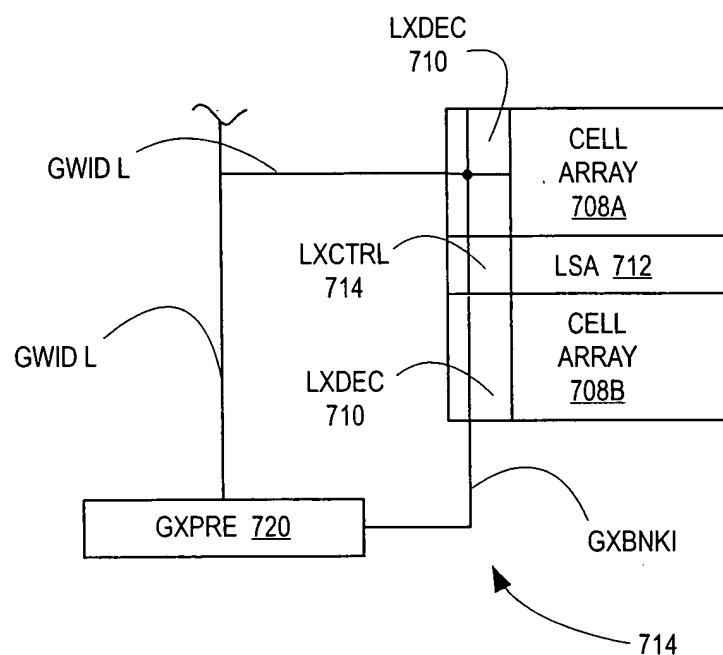


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FIG. 18



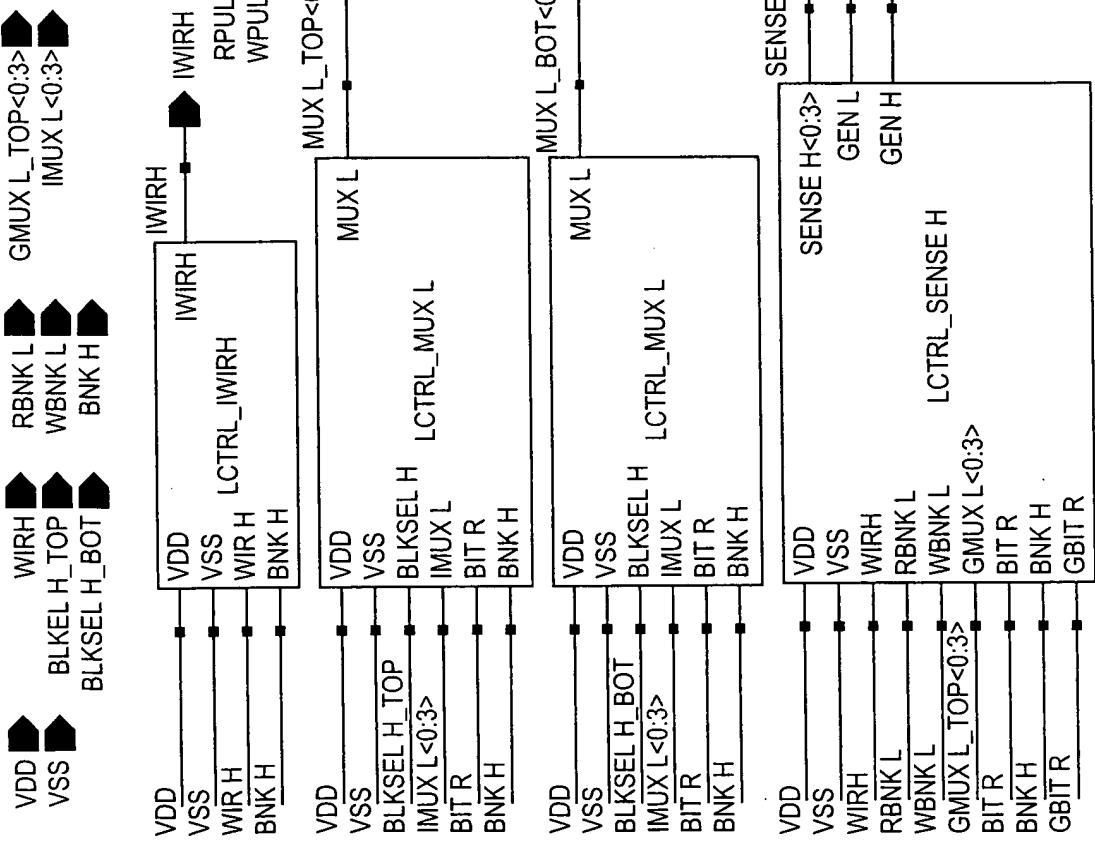
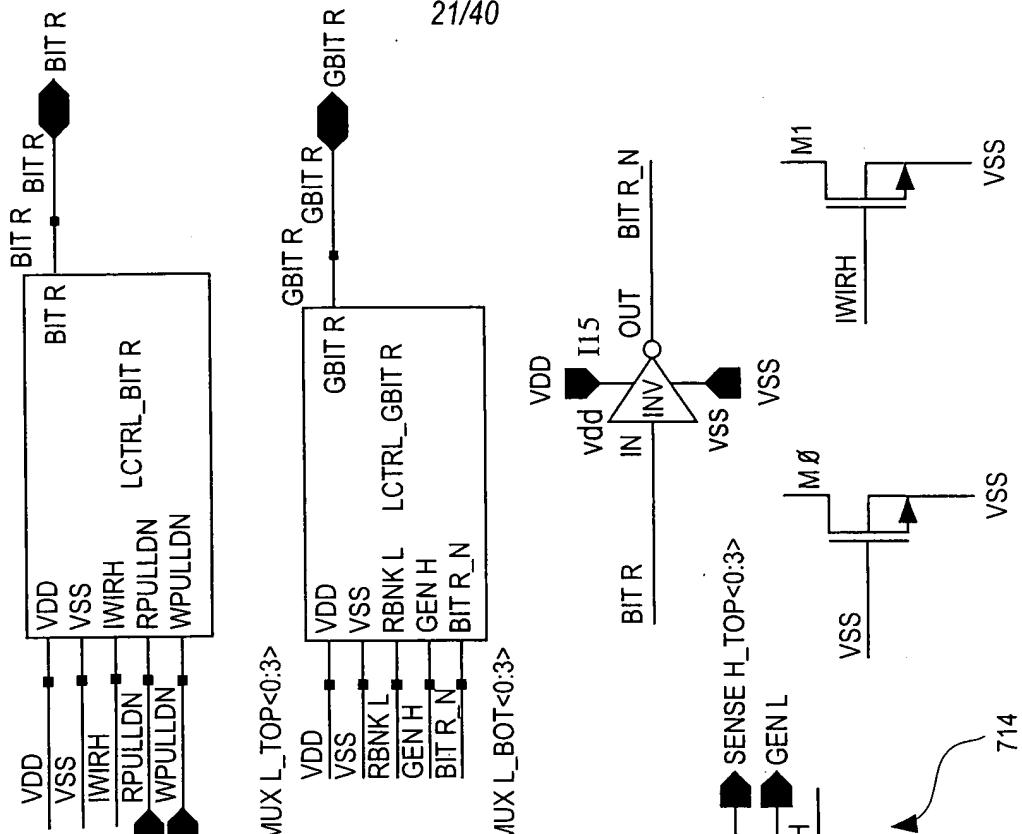
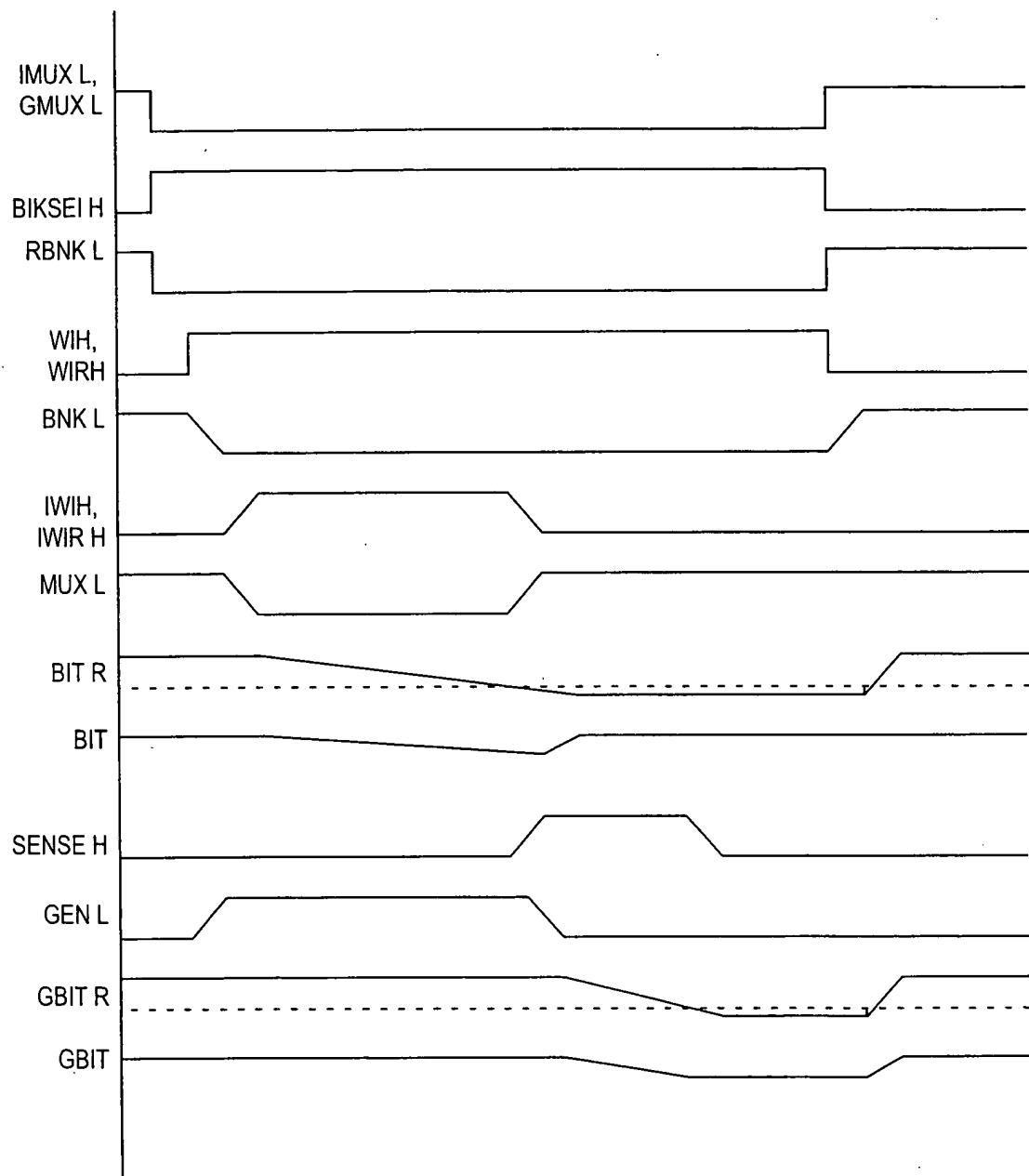


FIG. 19



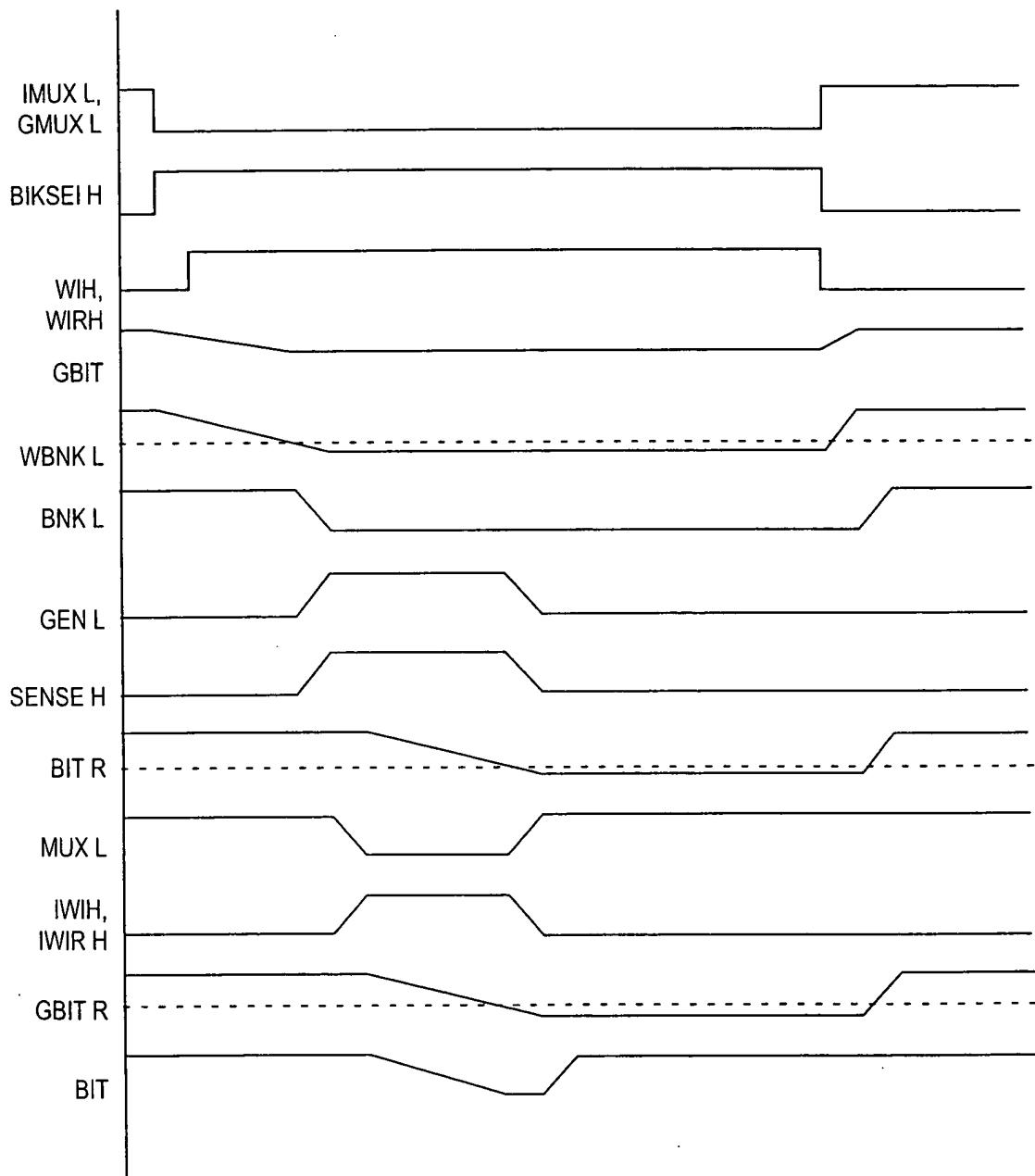
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FIG. 20



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FIG. 21



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FIG. 22A

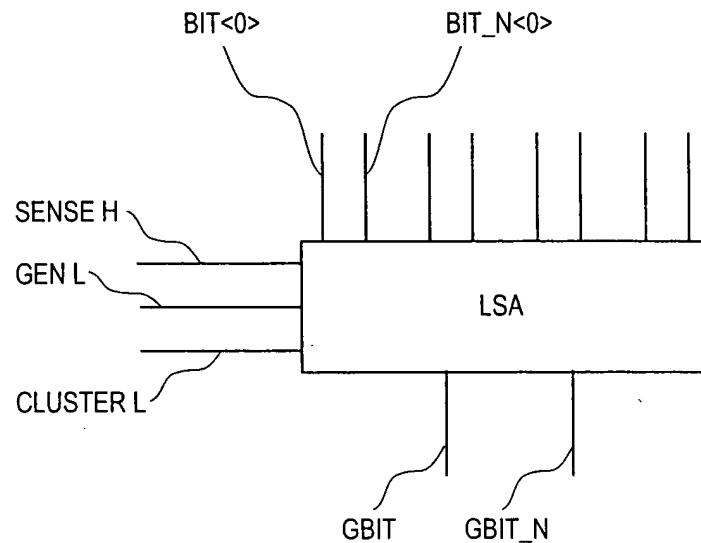


FIG. 22B

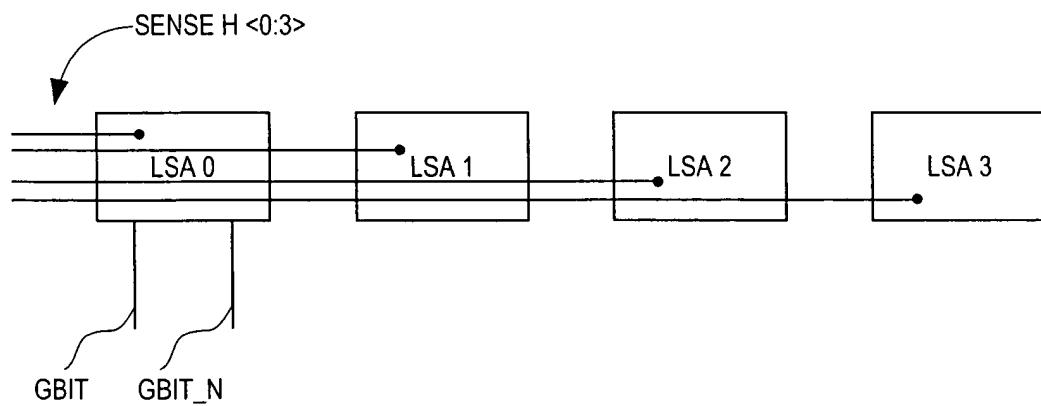
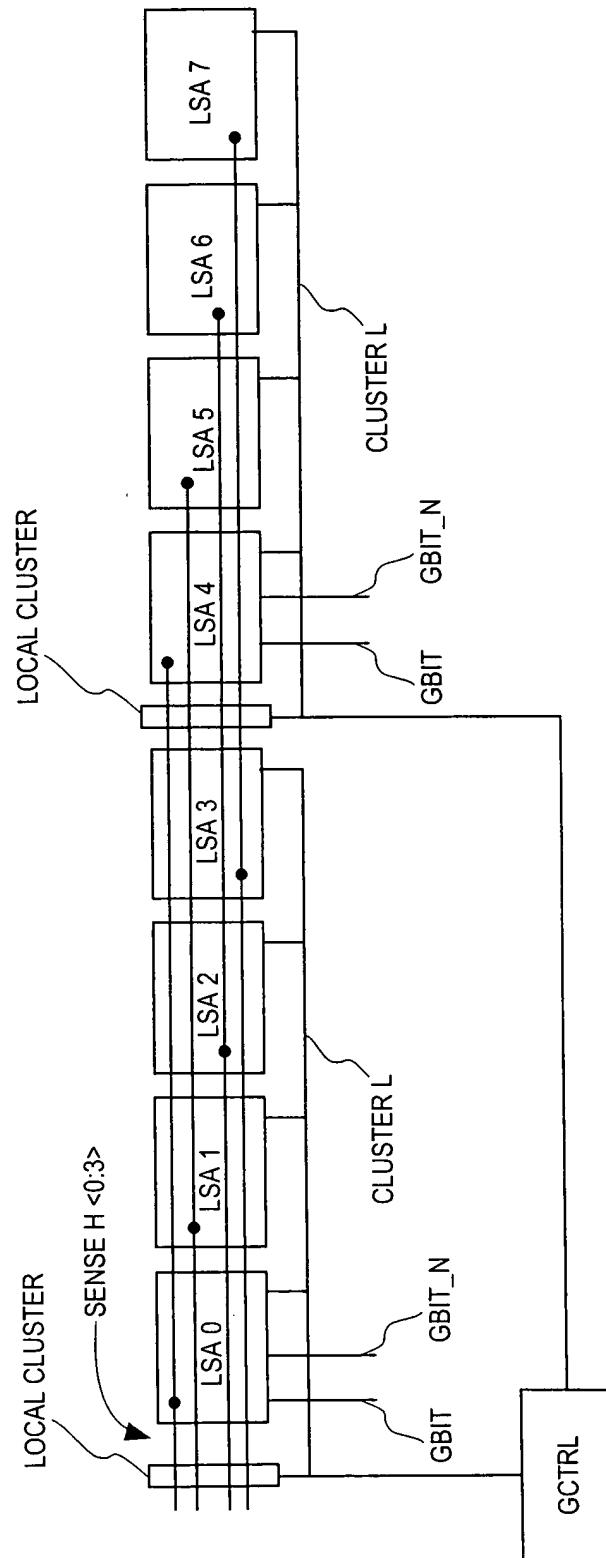


FIG. 22C



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FIG. 23

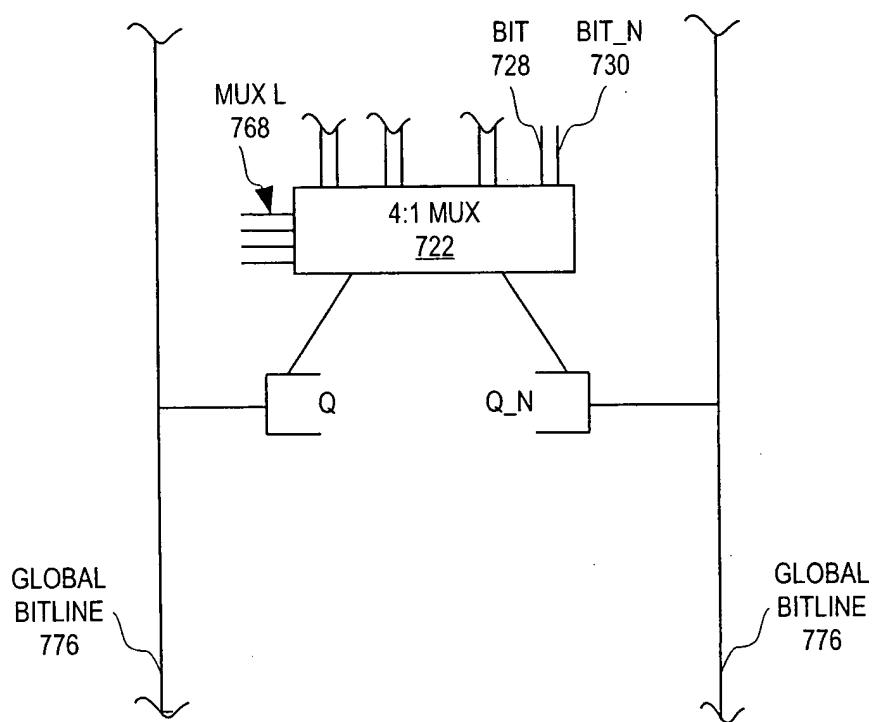
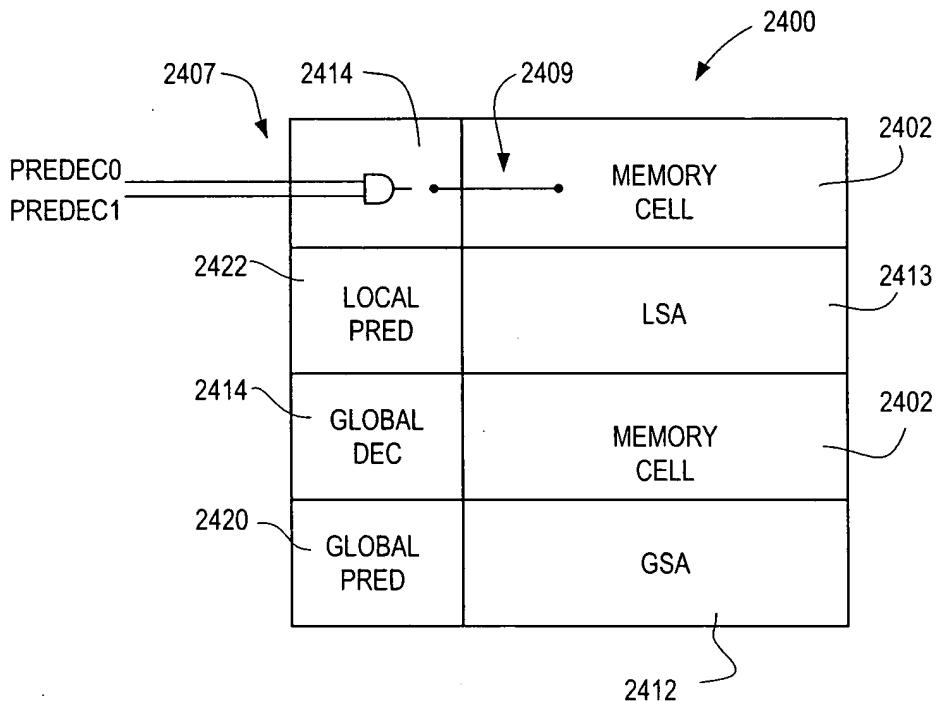


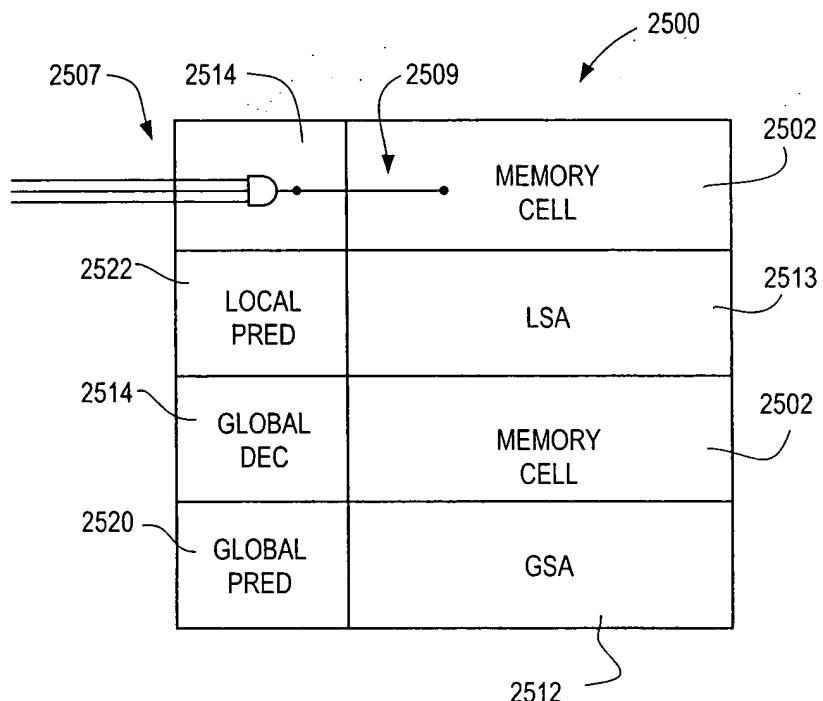
FIG. 24



TITLE: Block Redundancy Implementation In
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ATTORNEY: TLH, PHONE: 312-775-8000

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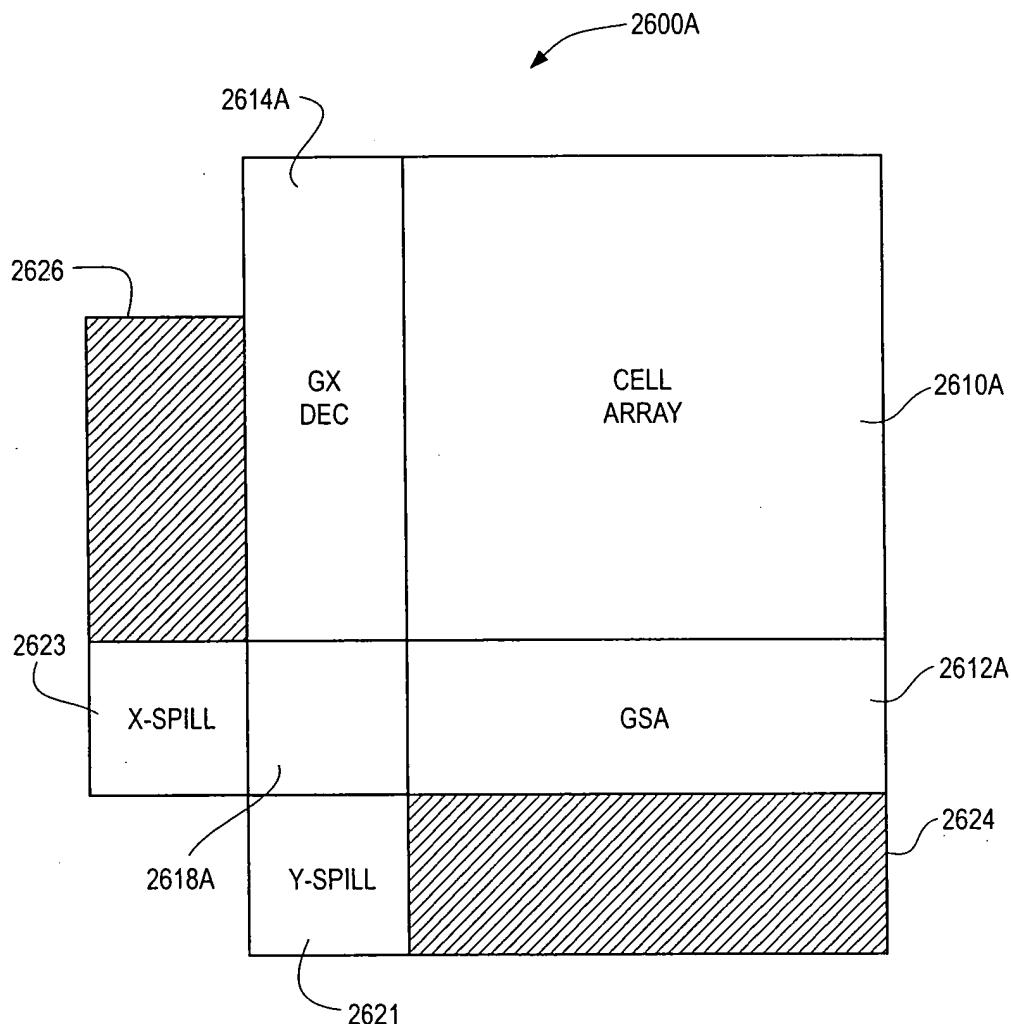
FIG. 25



TITLE: Block Redundancy Implementation In
Hierarchical RAM's
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APPLICATION NO.: unassigned,
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ATTORNEY: TLH, PHONE: 312-775-8000

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FIG. 26A



TITLE: Block Redundancy Implementation In
Hierarchical RAM's
INVENTOR: Terzioglu, et al.
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FIG. 26B

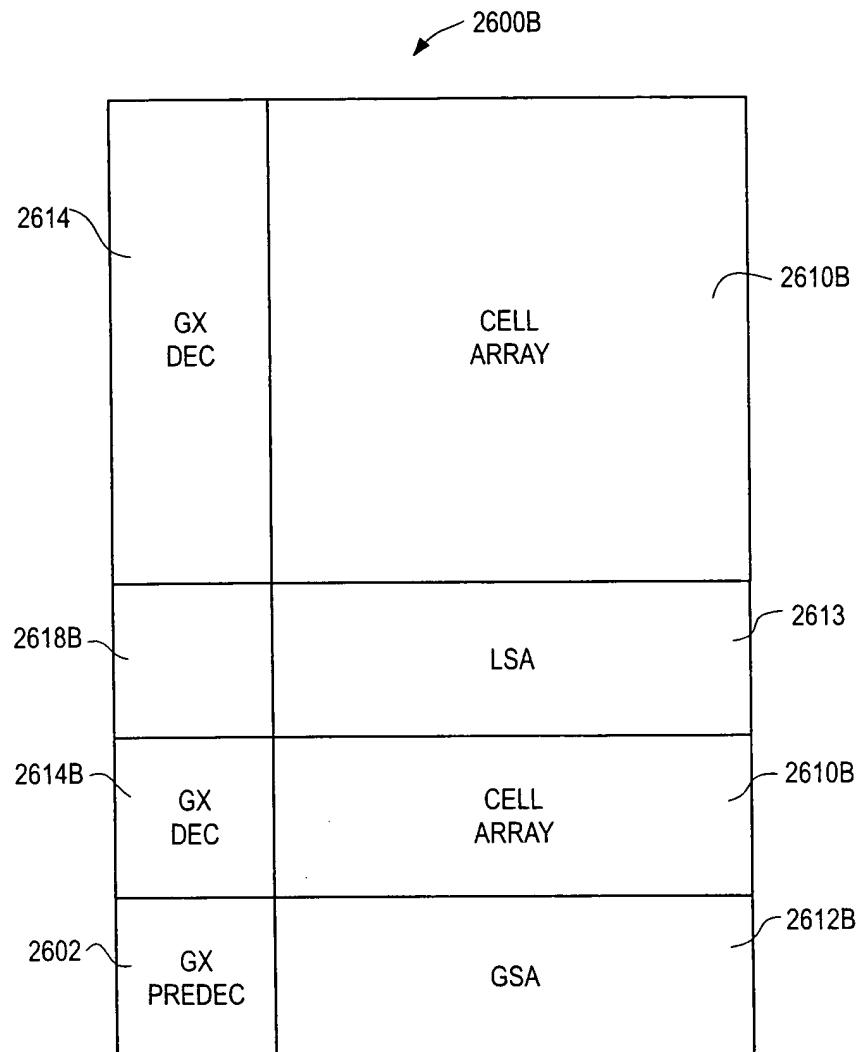


FIG. 27

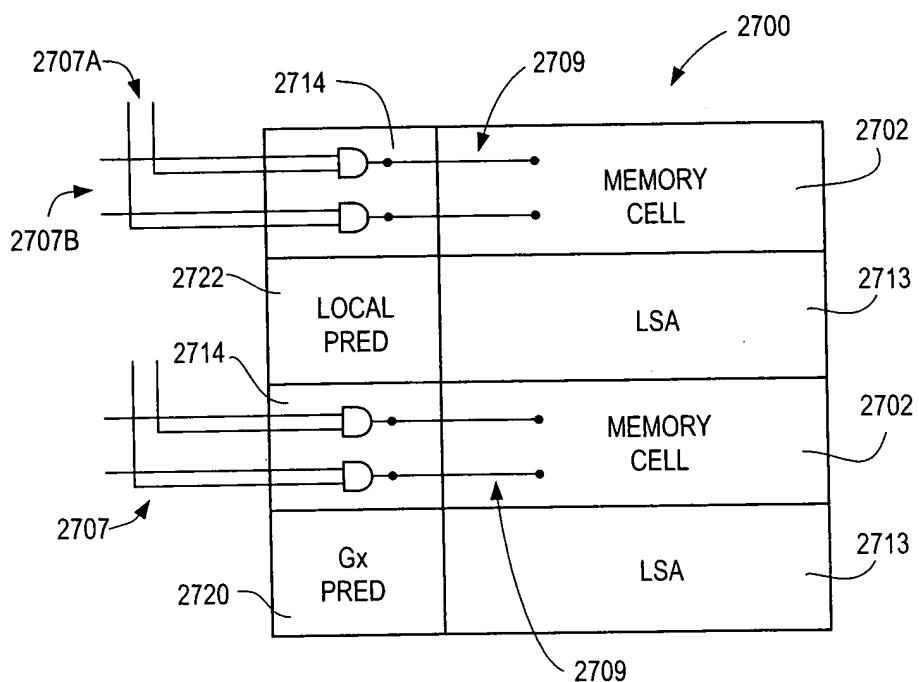


FIG. 28

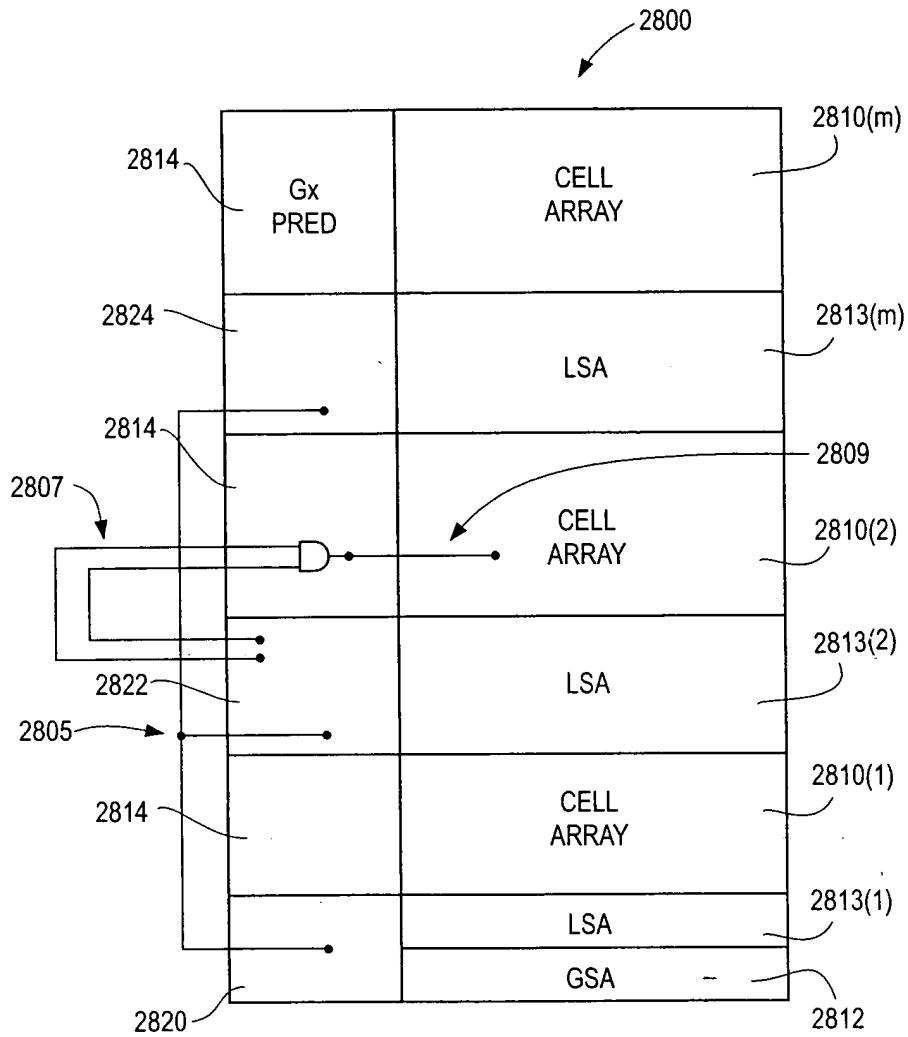
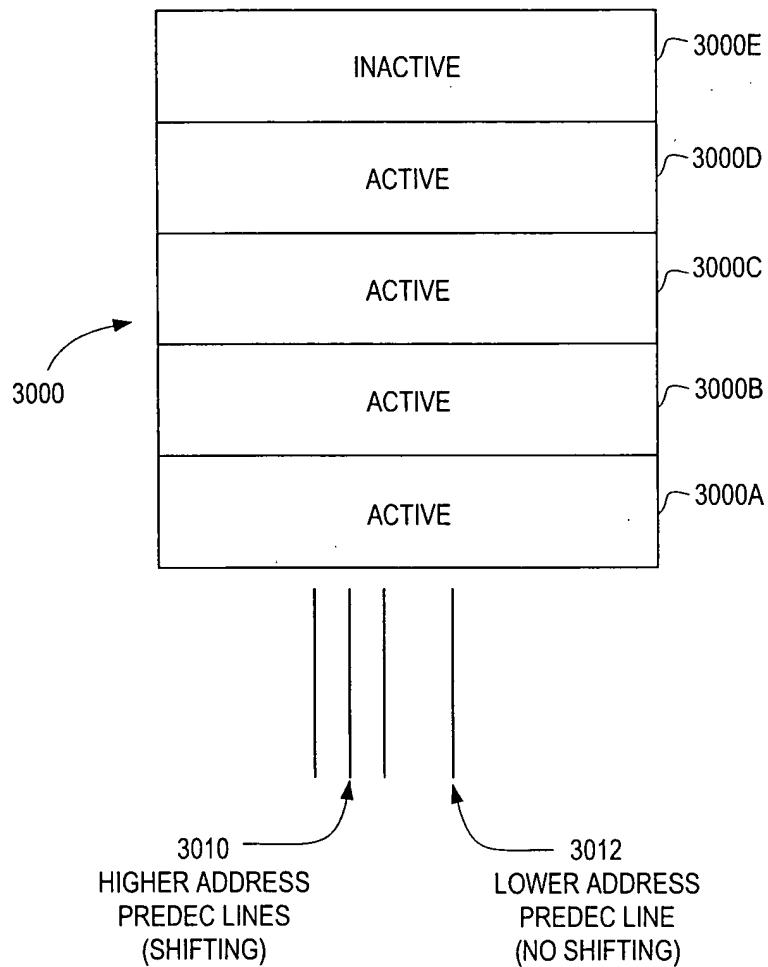


FIG. 29



TITLE: Block Redundancy Implementation In
Hierarchical RAM's
INVENTOR: Terzioglu, et al.
APPLICATION NO.: unassigned,
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FIG. 30

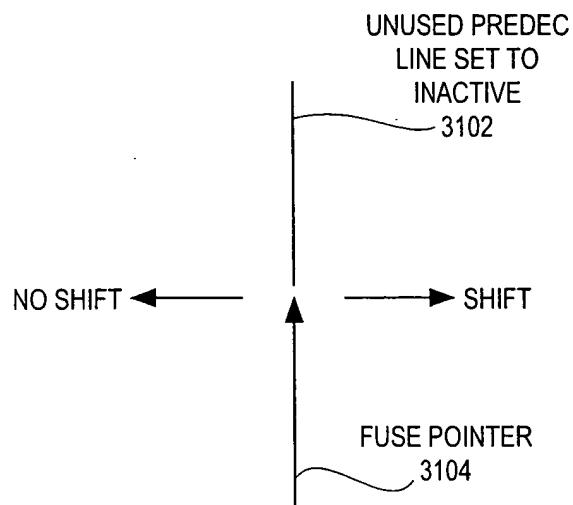


FIG. 32

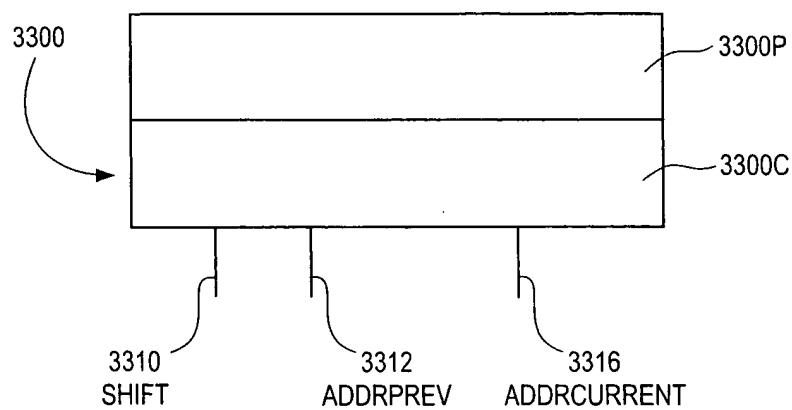


FIG. 31A

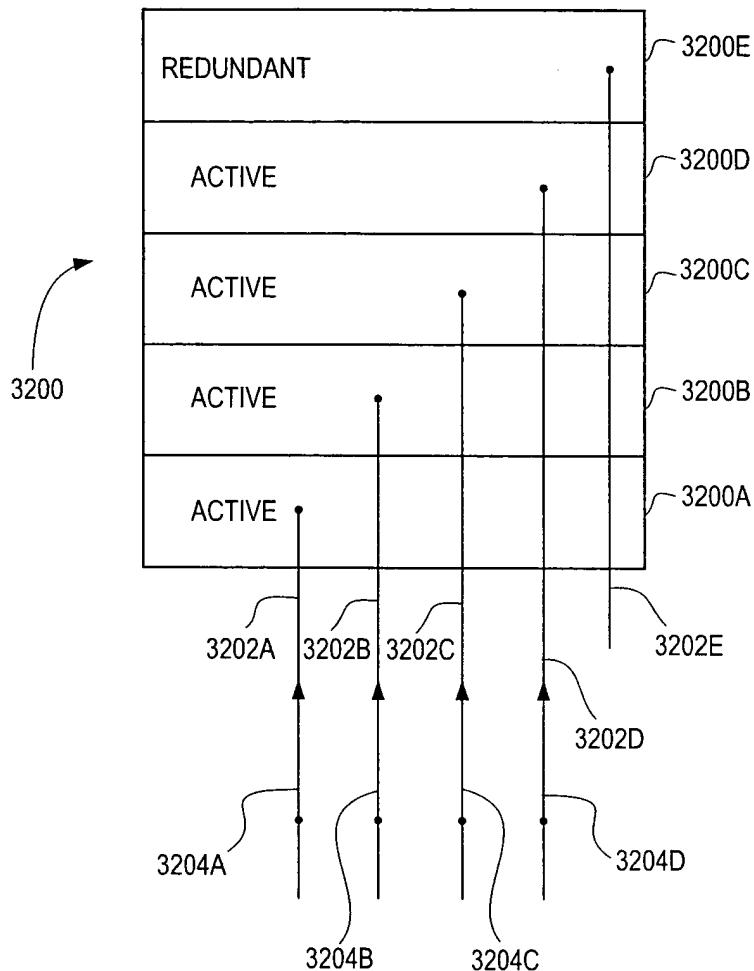
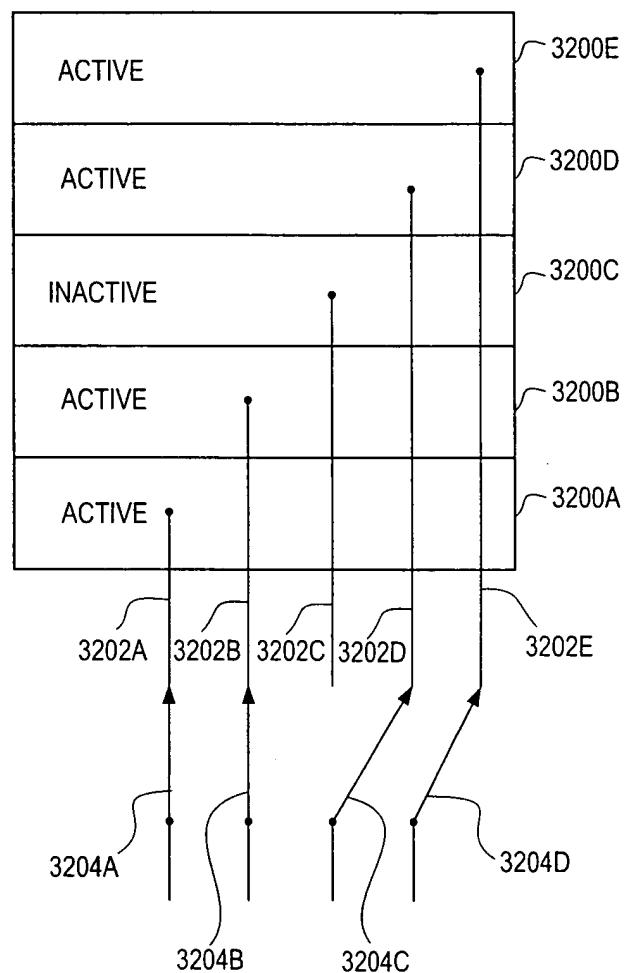


FIG. 31B



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FIG. 33A

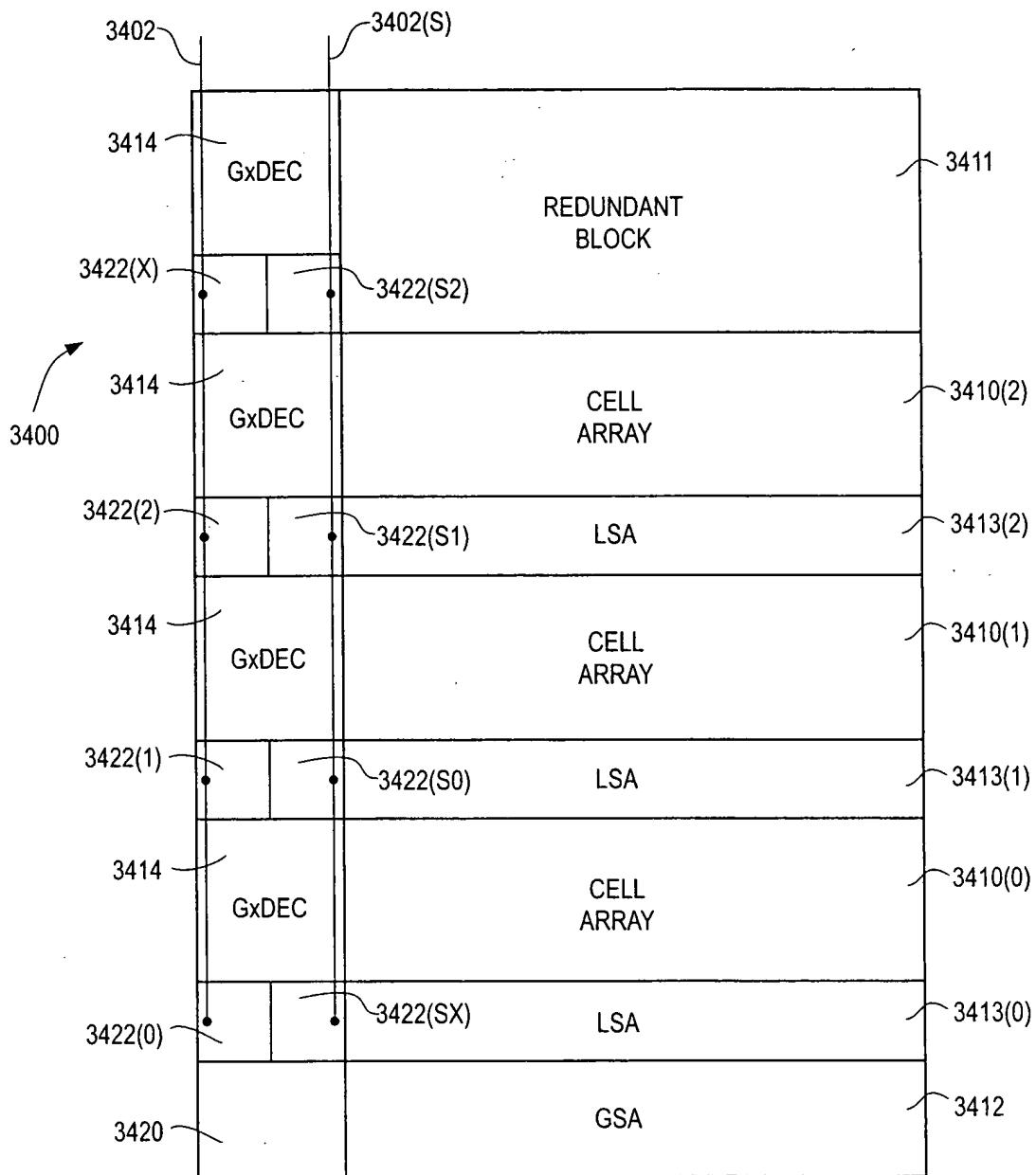
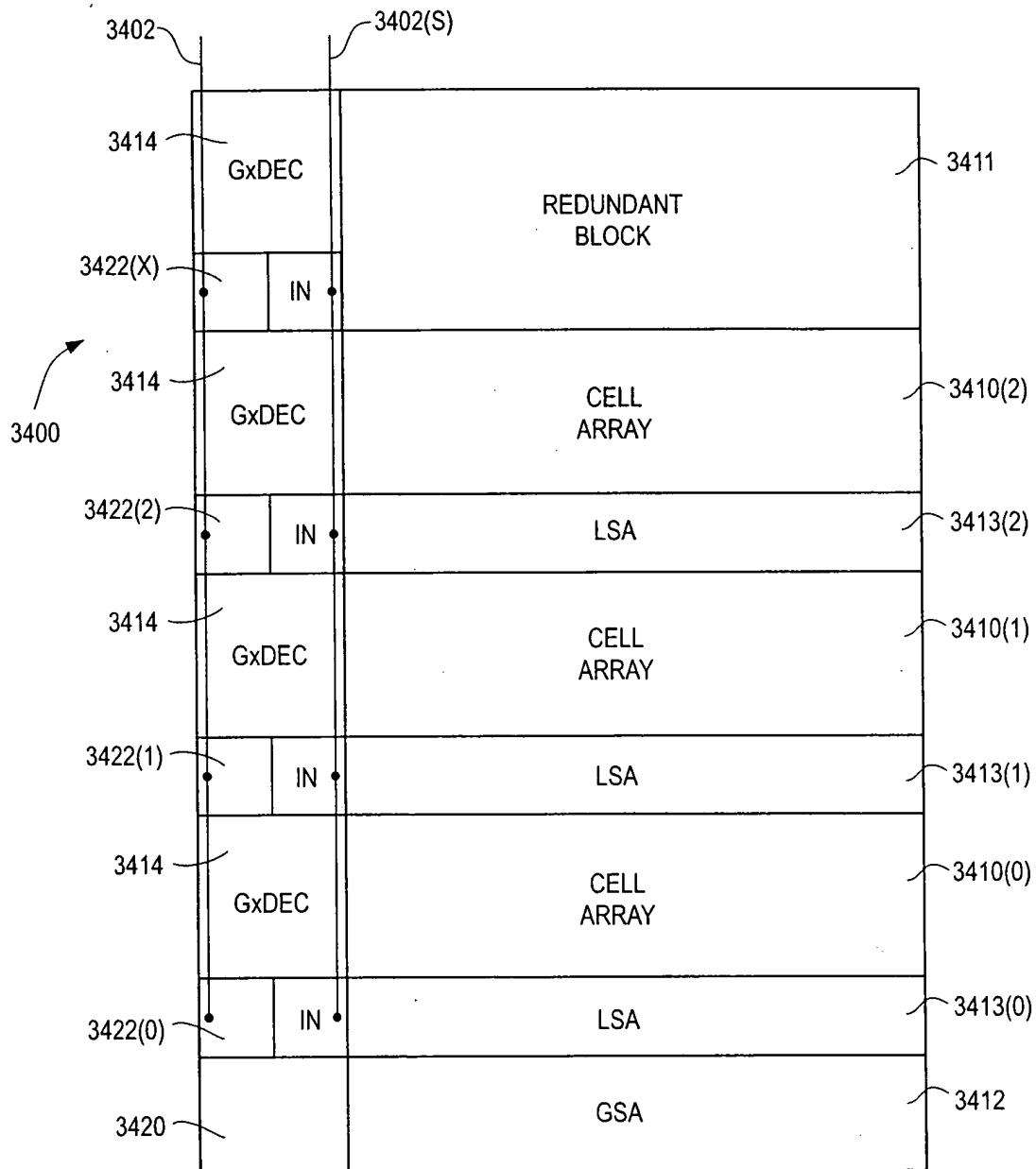


FIG. 33B



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FIG. 33C

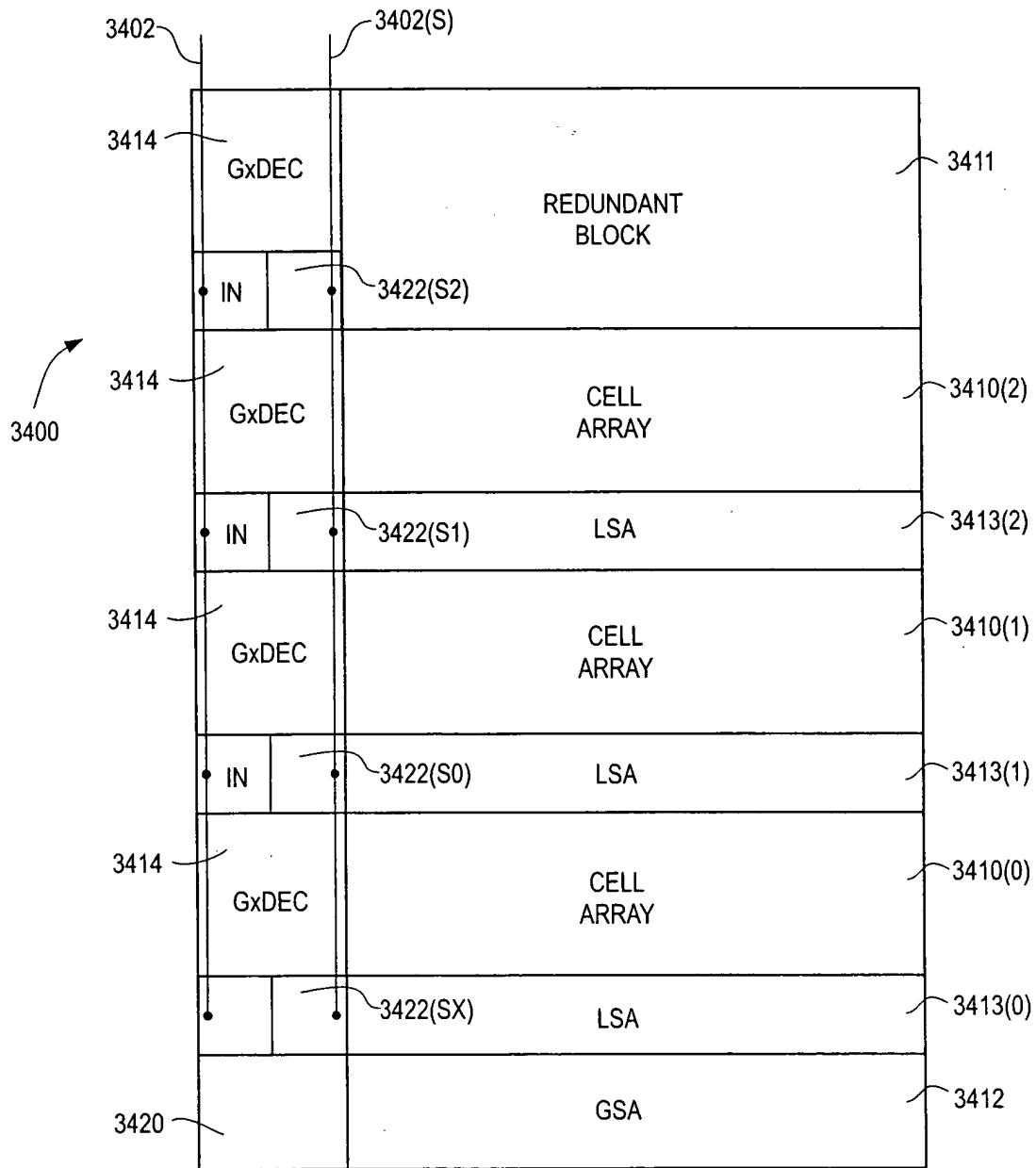


FIG. 34

